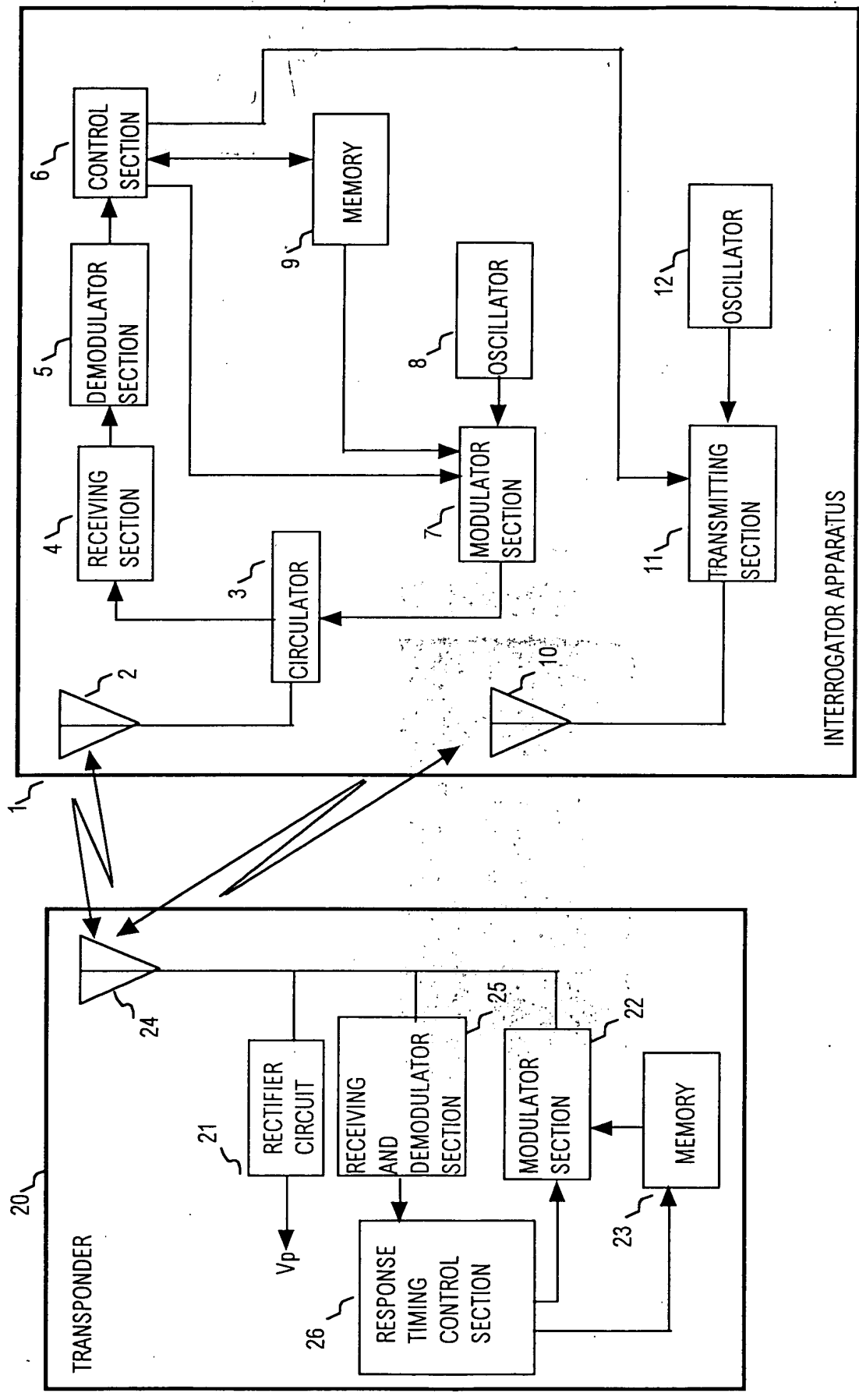


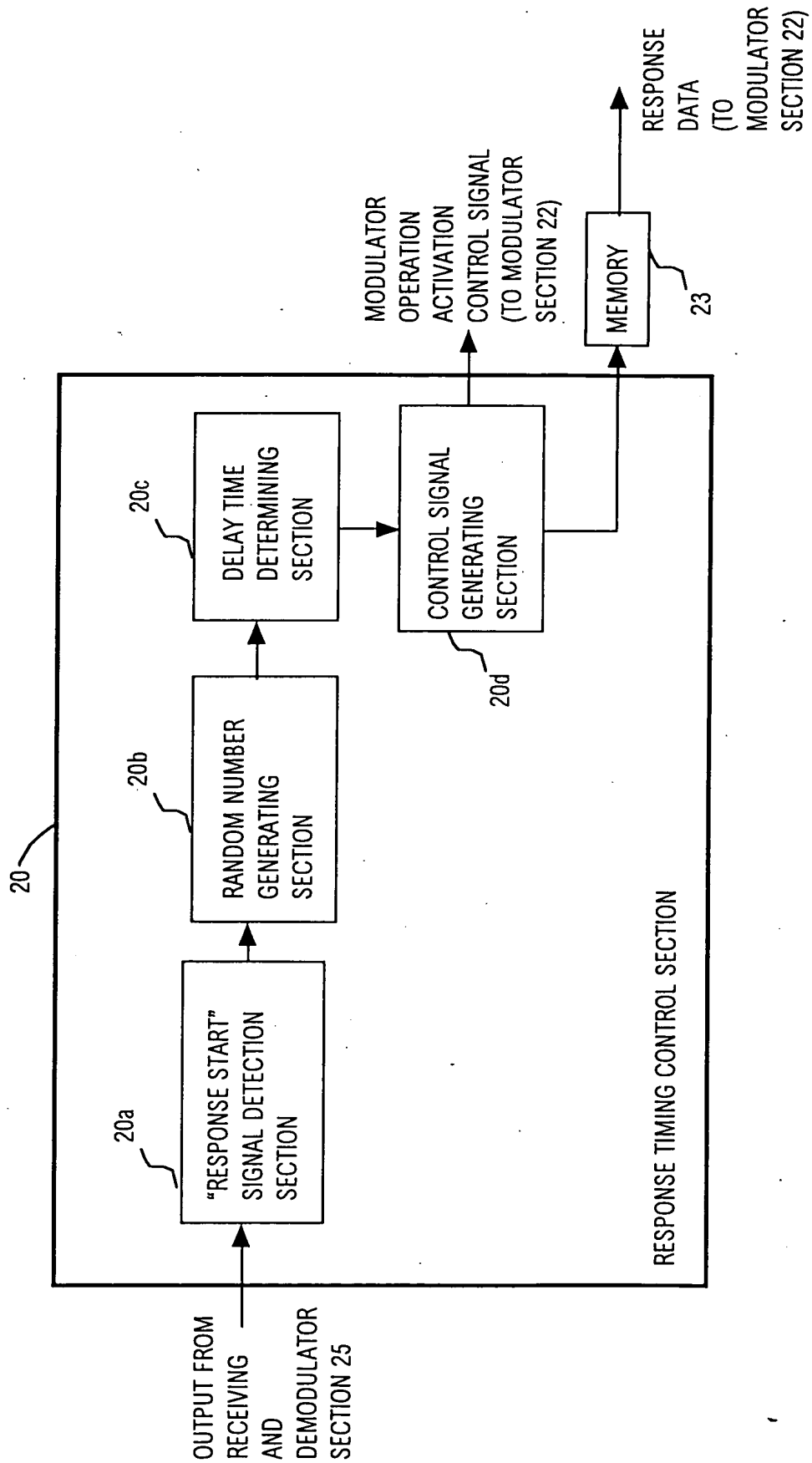
FIG. 1



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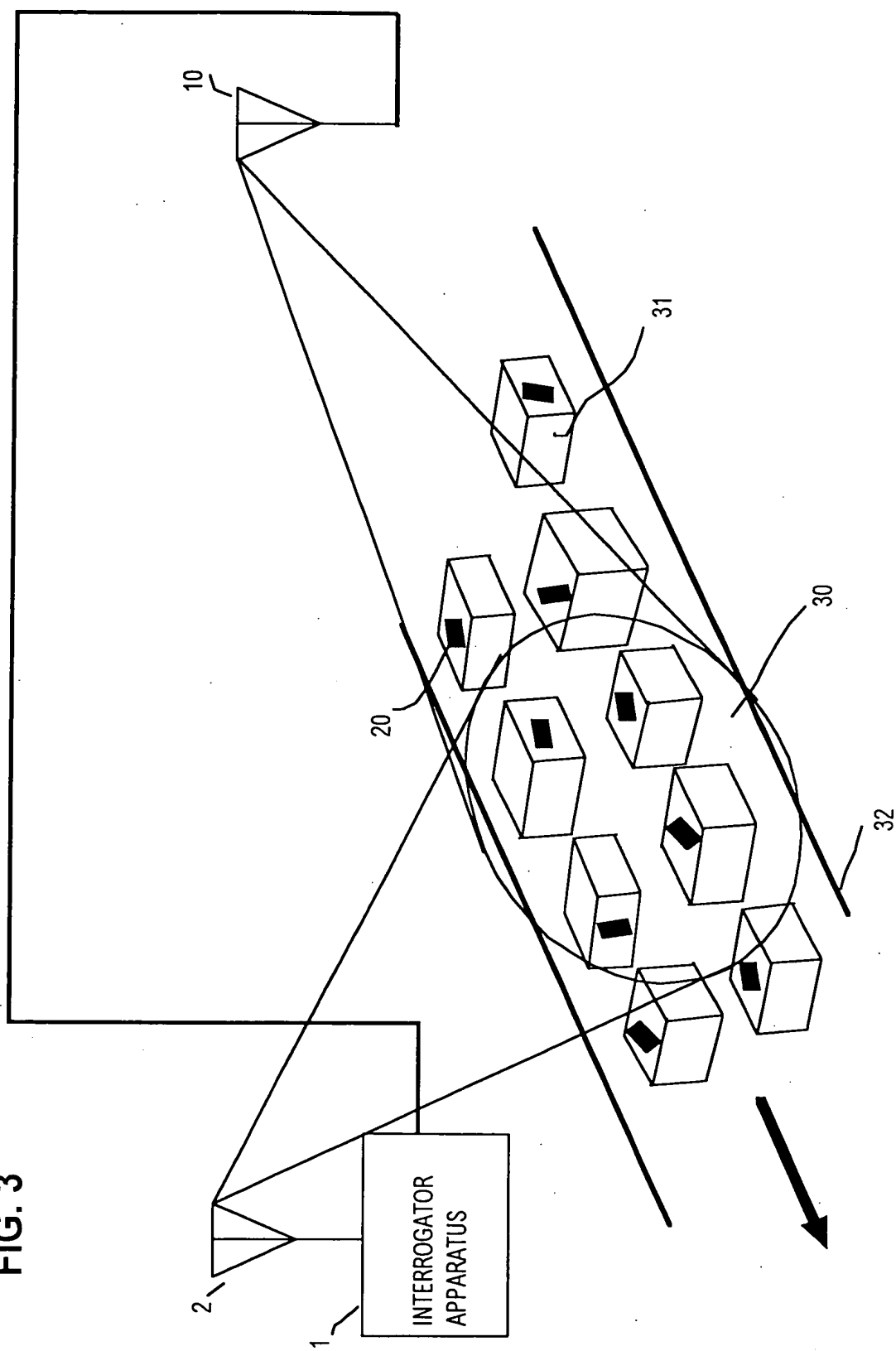
FIG. 2



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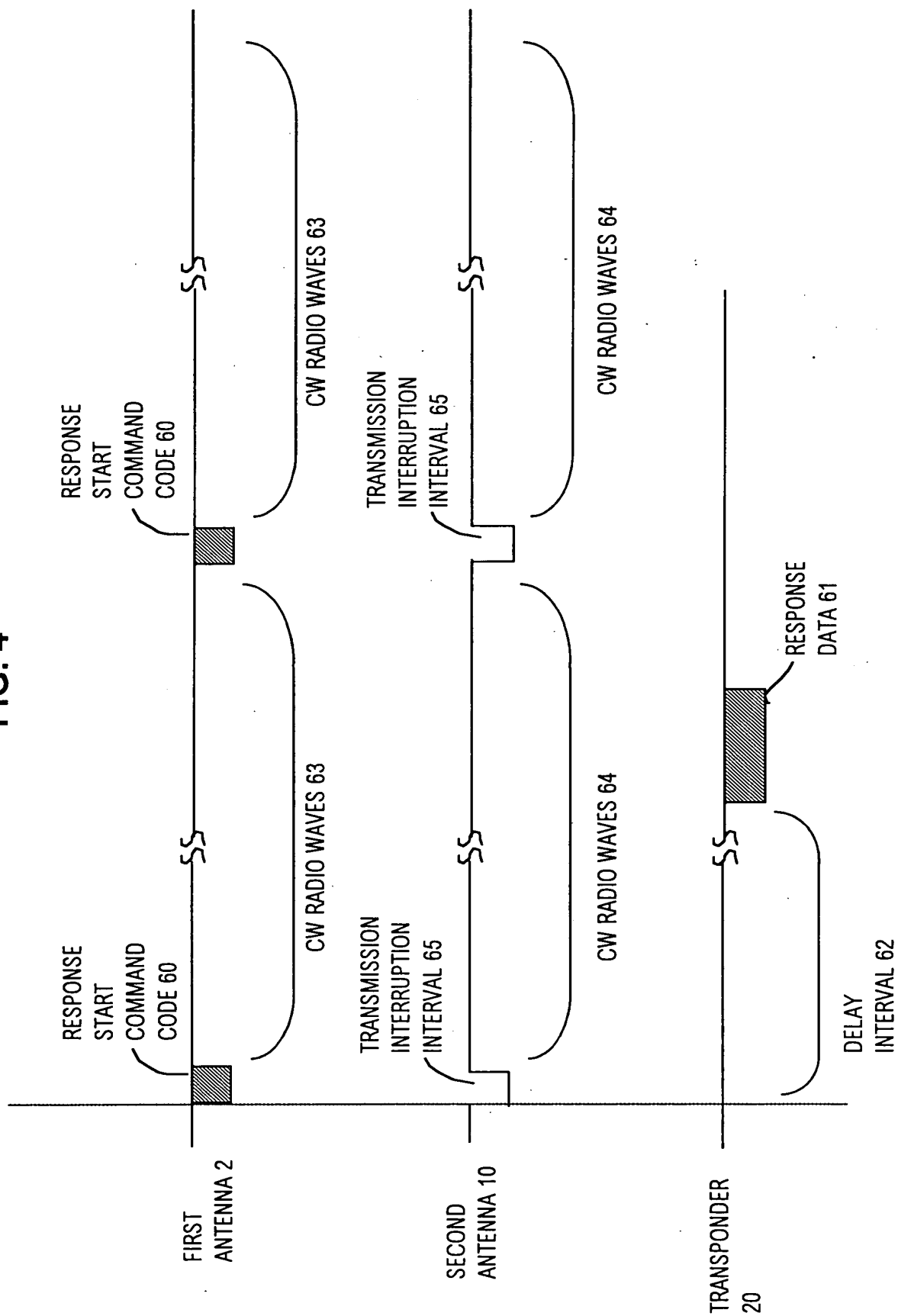
FIG. 3



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FIG. 4



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FIG. 6

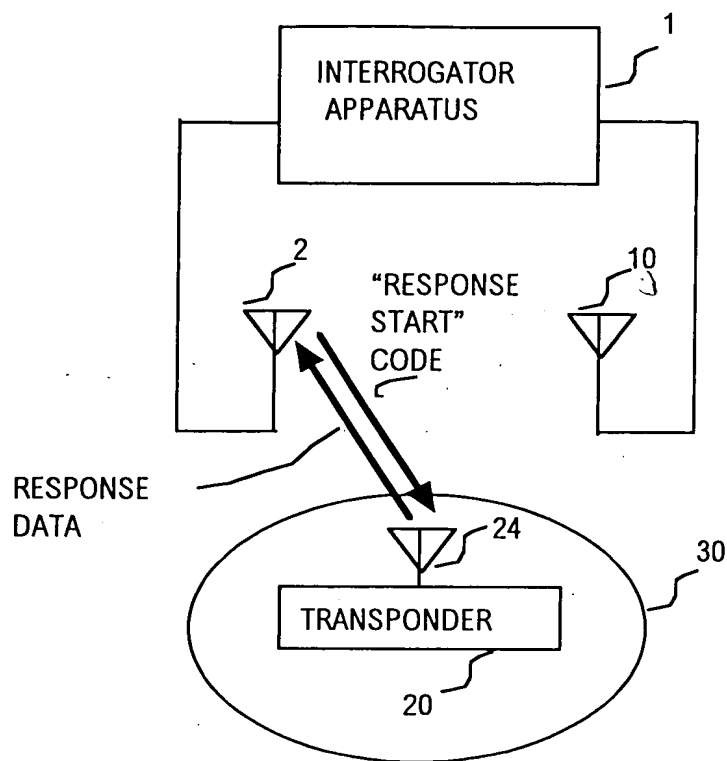
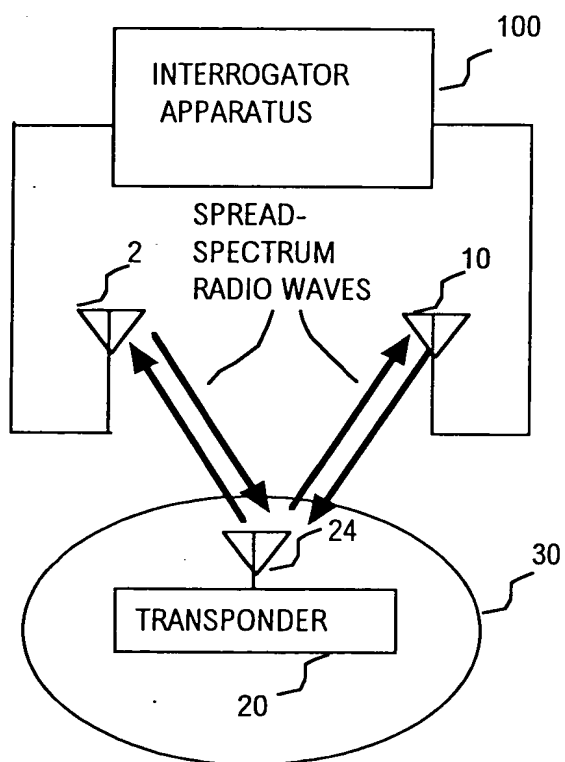
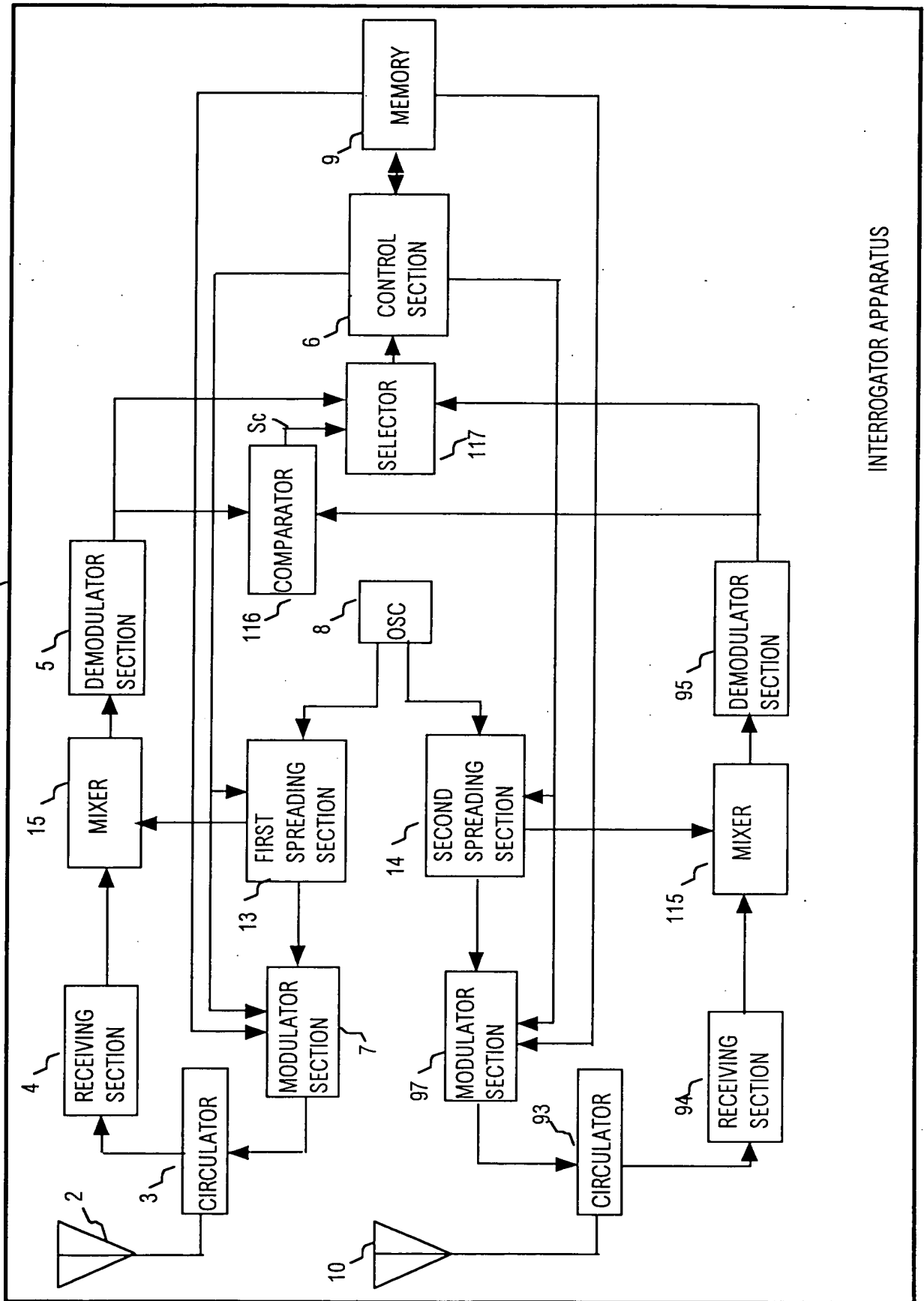


FIG. 7



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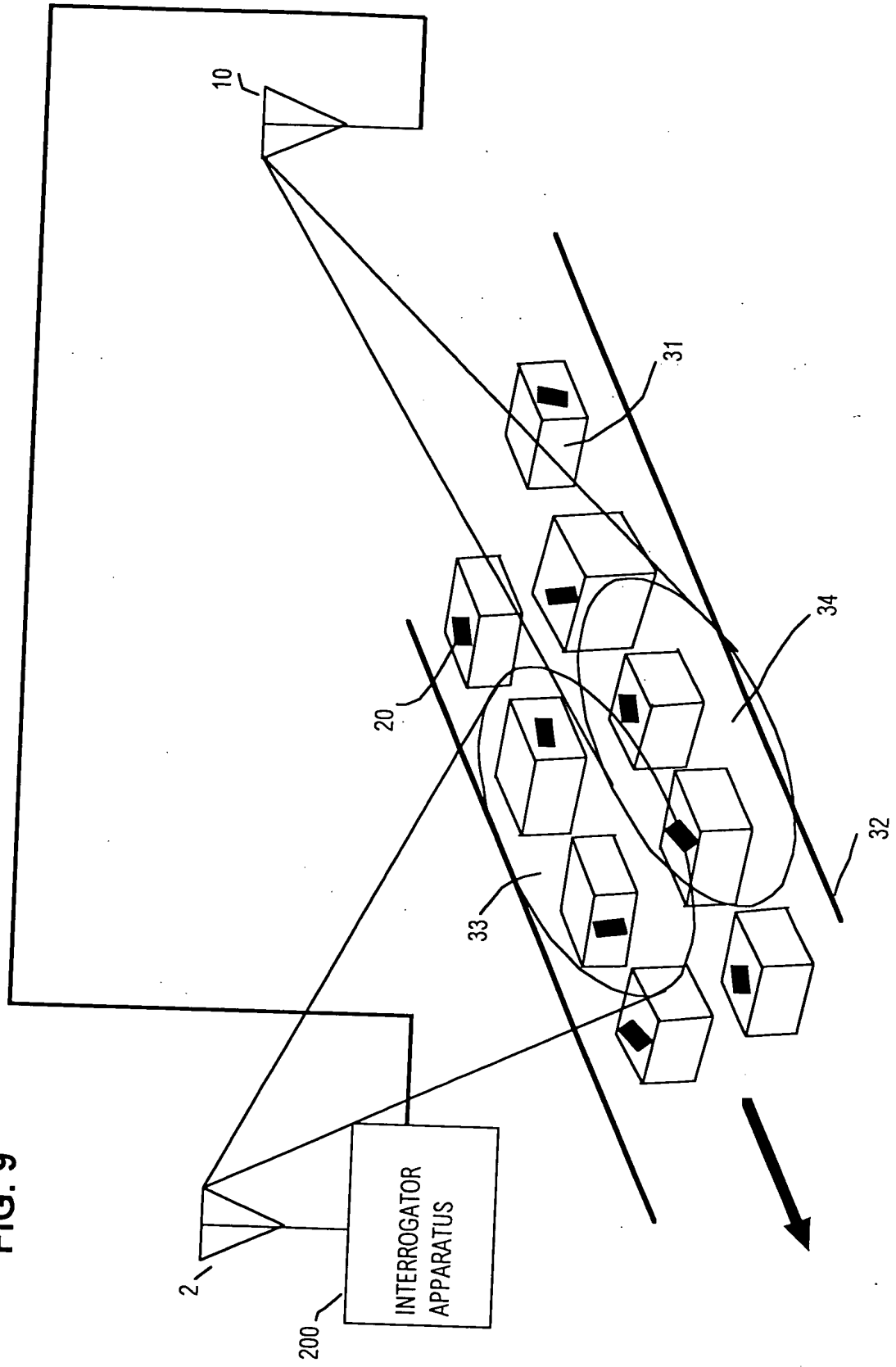
FIG. 8



INTERROGATOR APPARATUS

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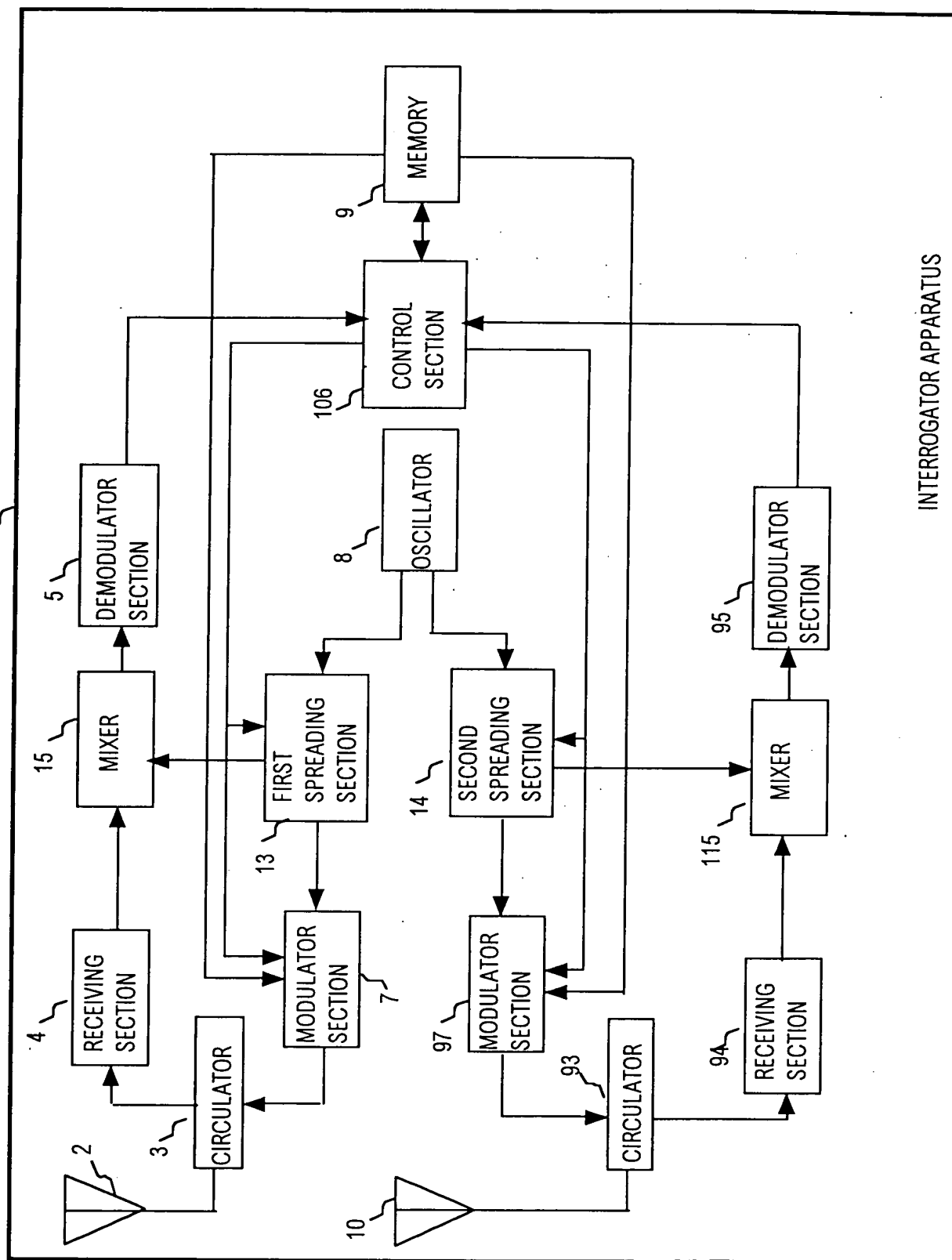
FIG. 9



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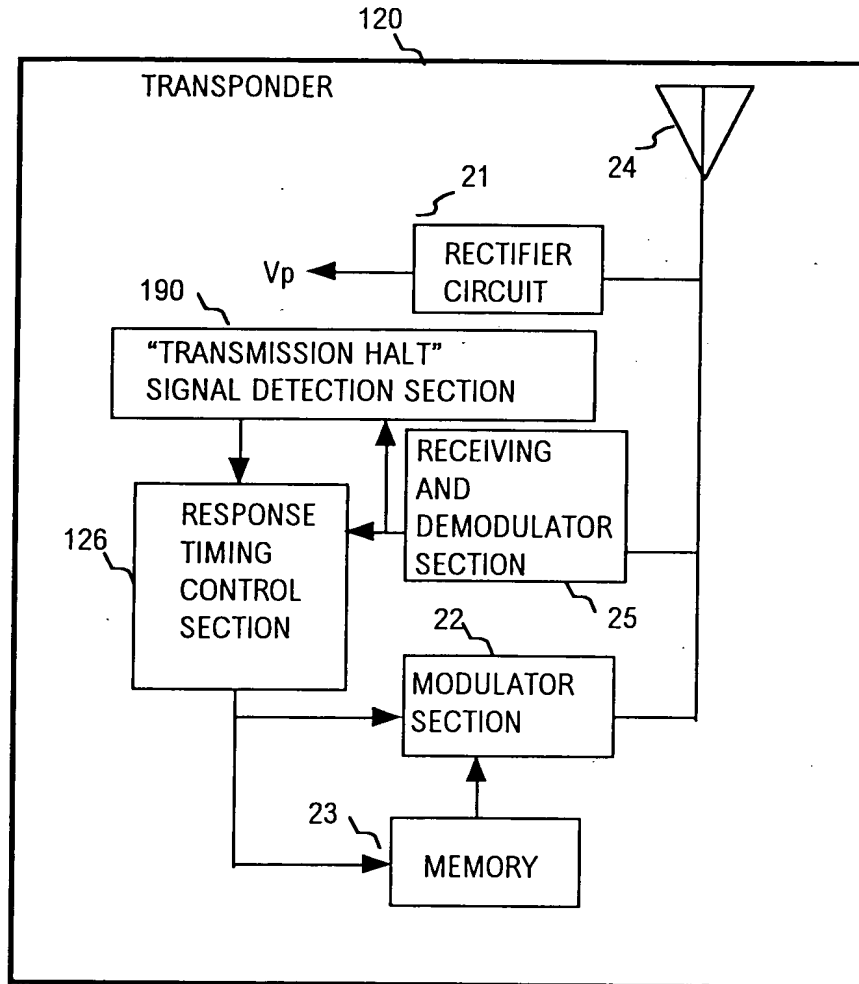
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INTERROGATOR APPARATUS

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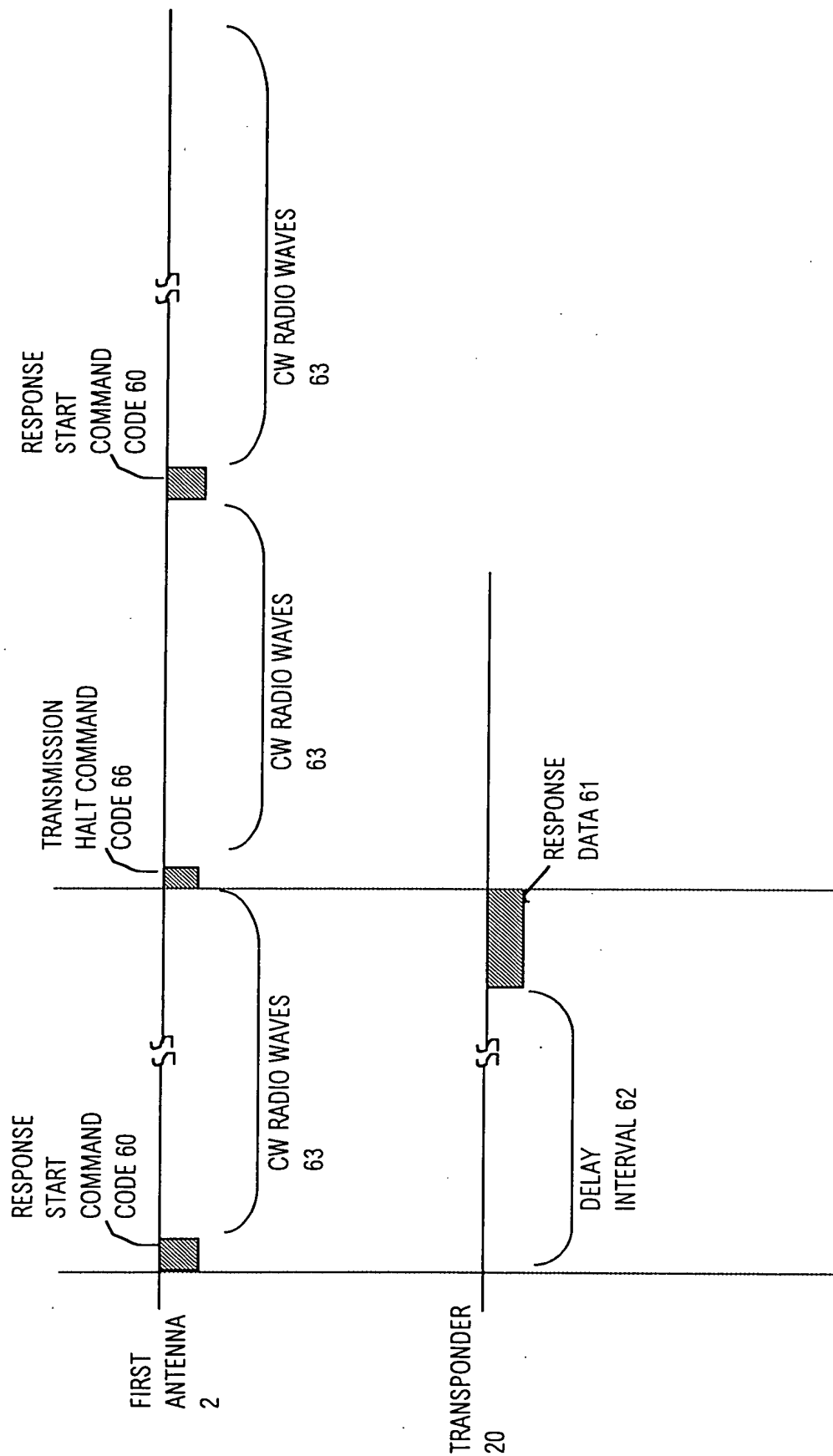
FIG. 11



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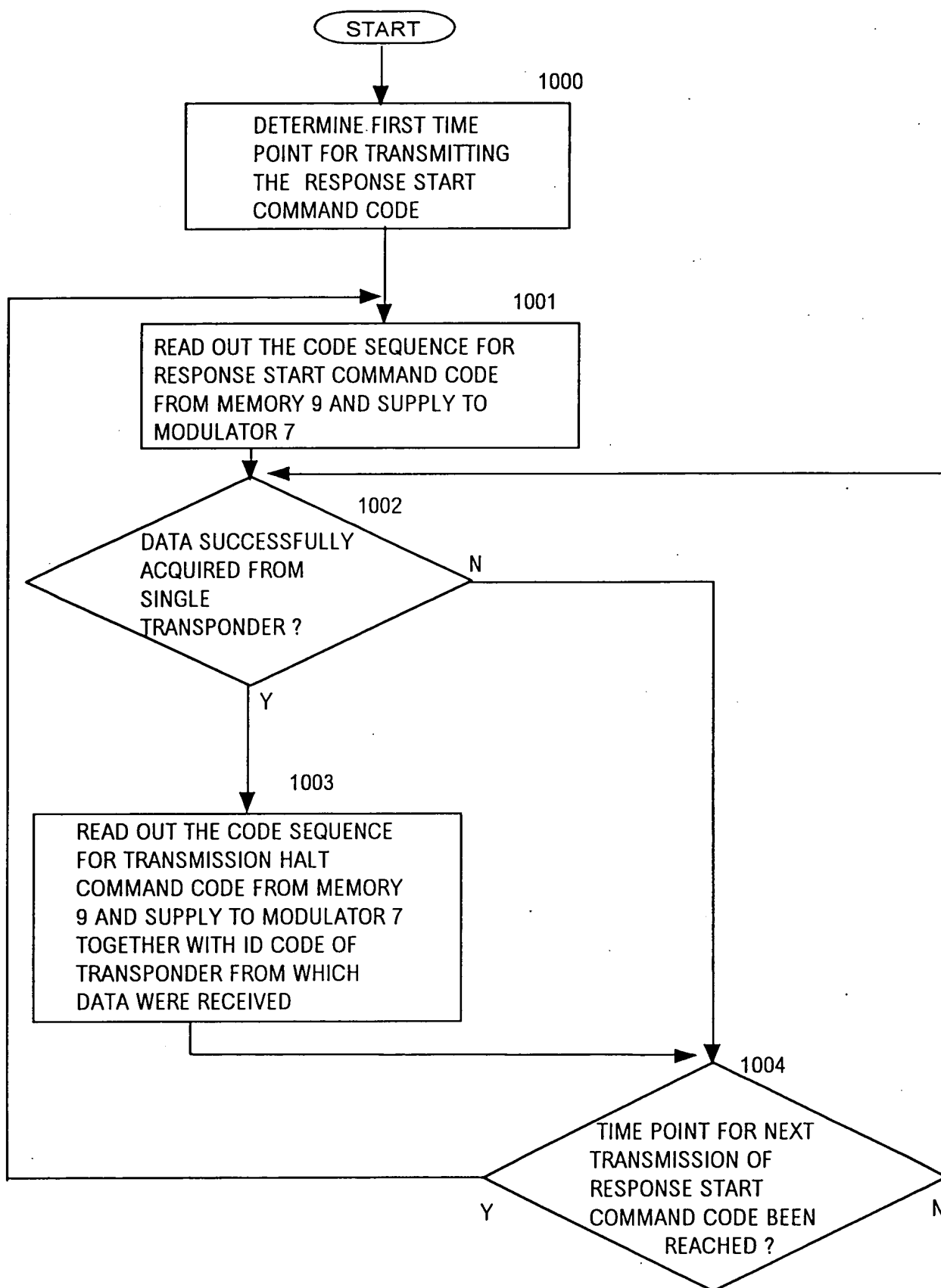
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FIG. 12



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FIG. 13



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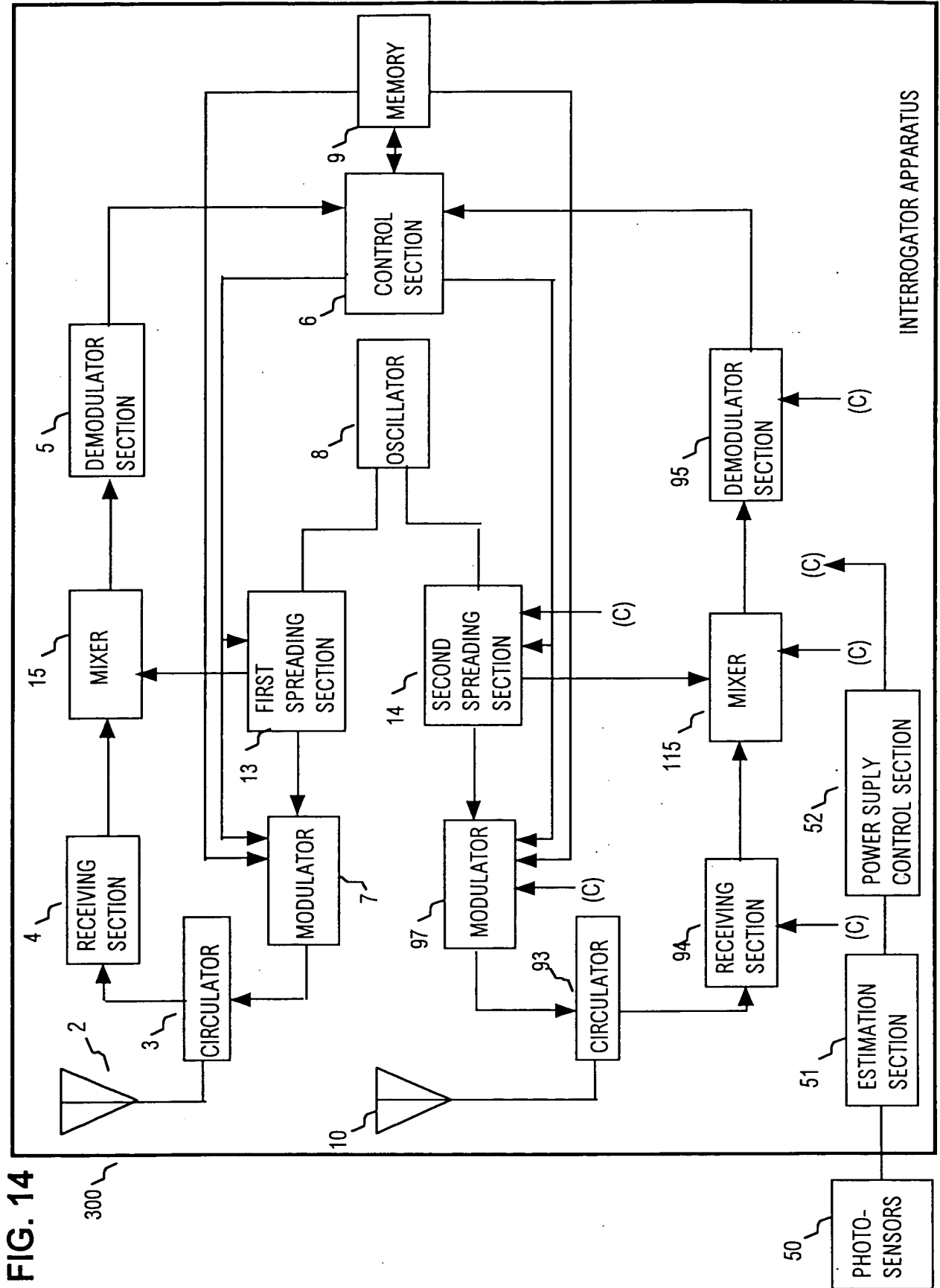


FIG. 15

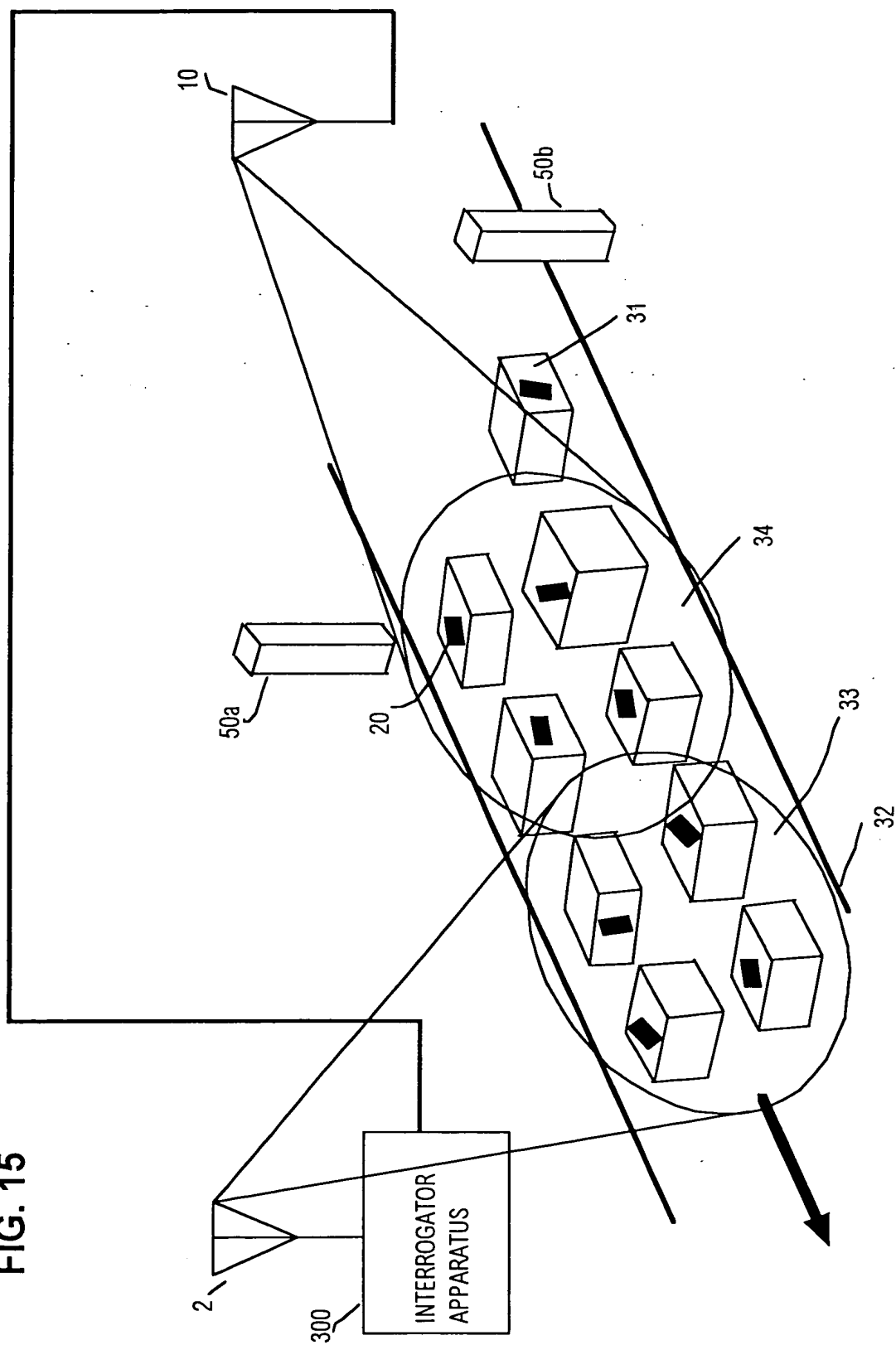


FIG. 16

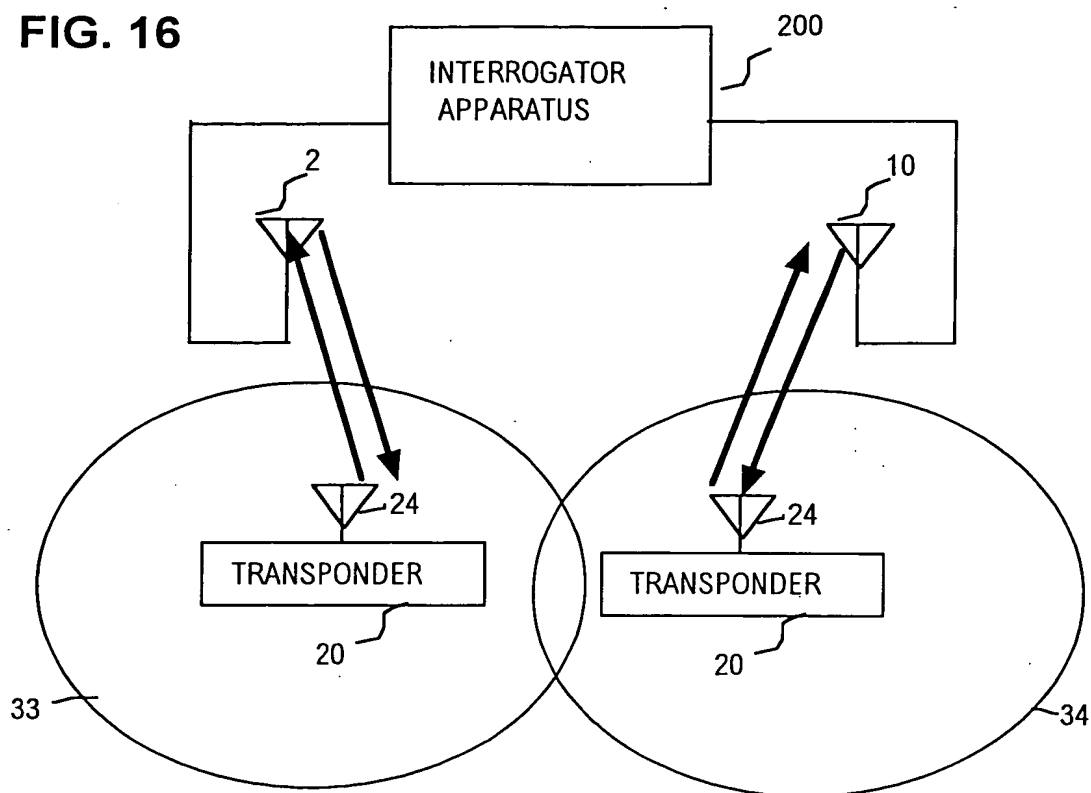


FIG. 17A

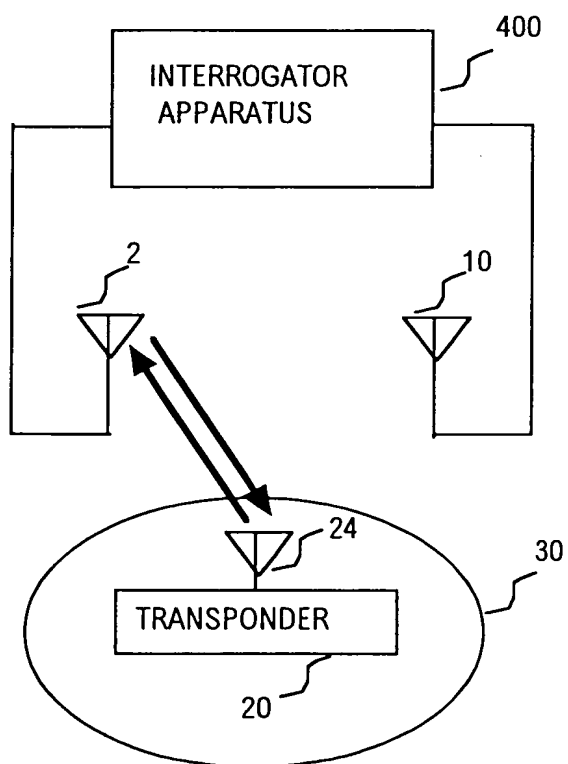


FIG. 17B

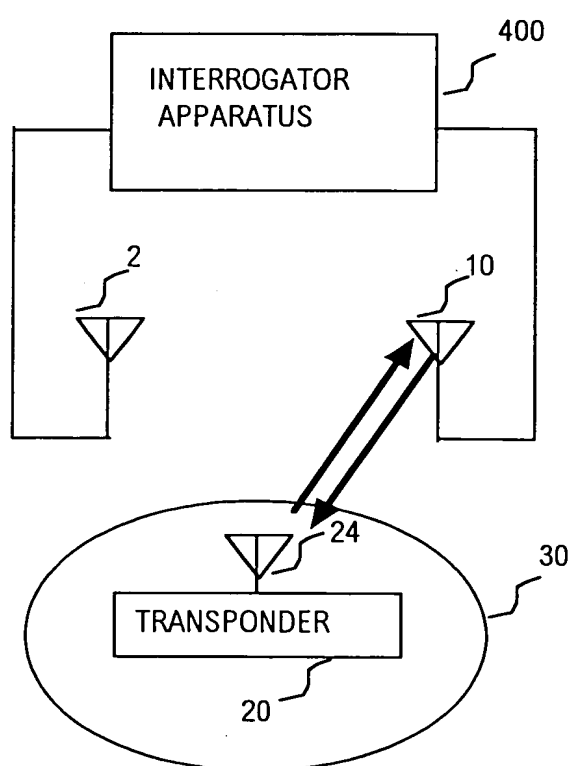
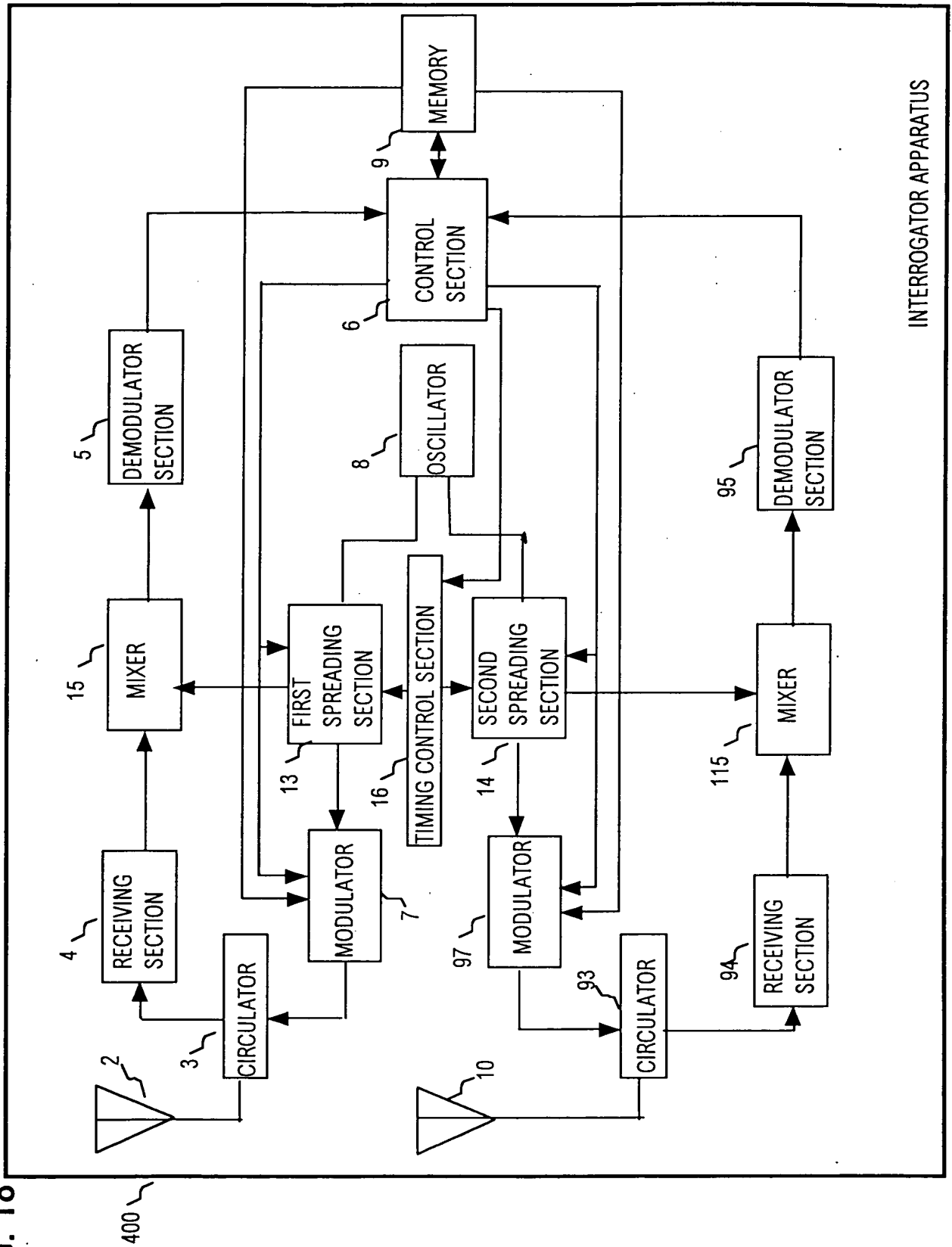


FIG. 18

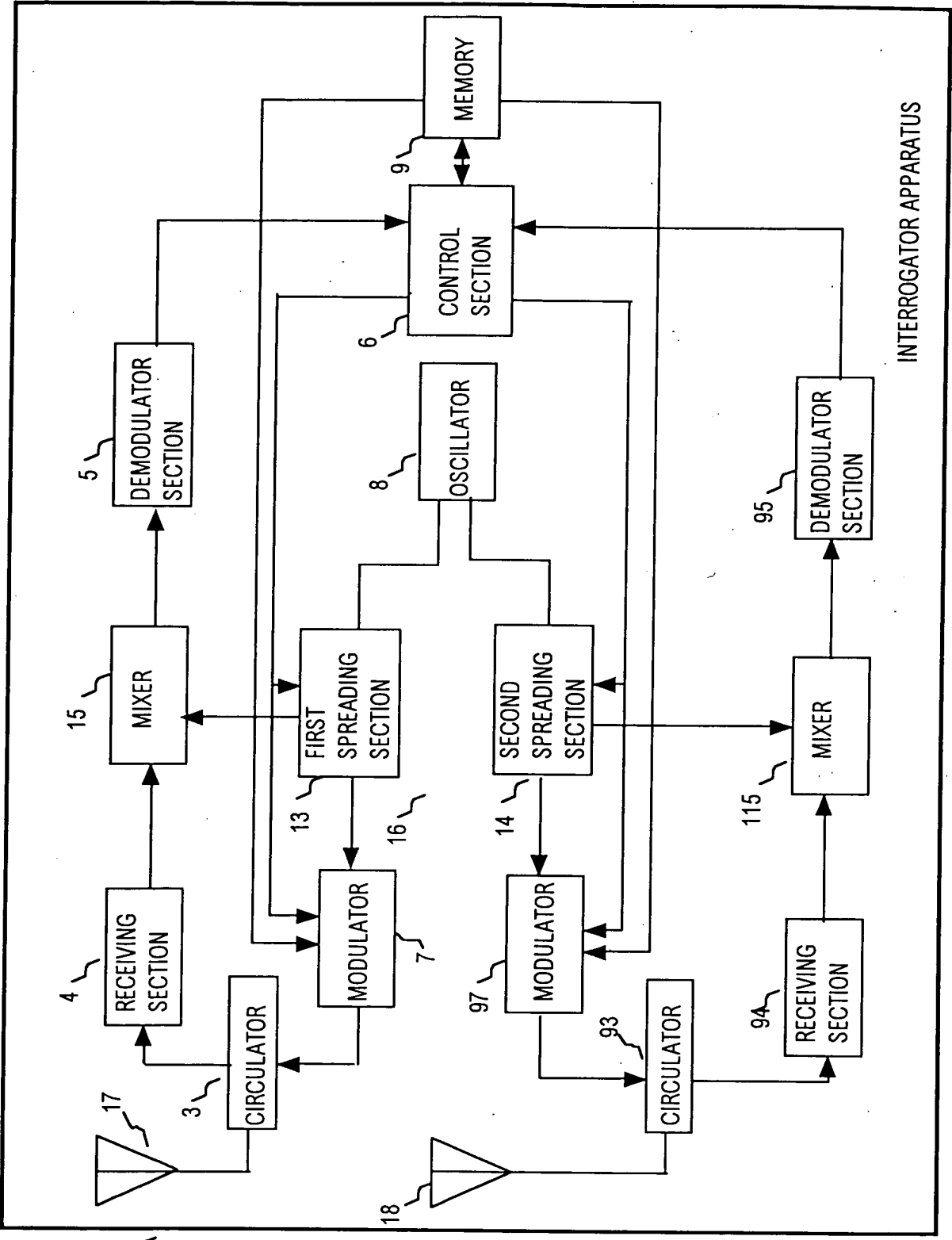


INTERROGATOR APPARATUS

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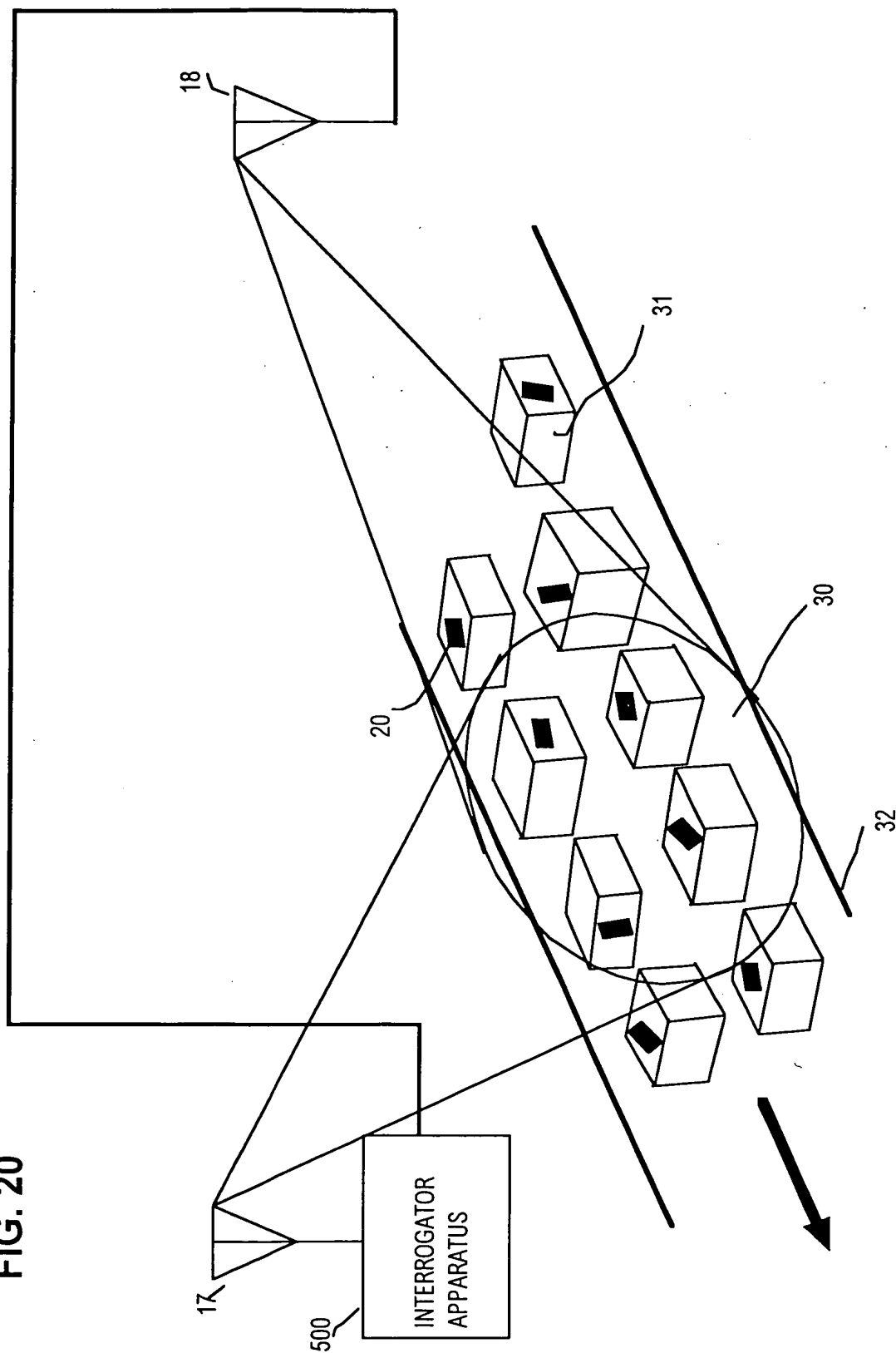
FIG. 19



INTERROGATOR APPARATUS

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FIG. 20



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The diagram illustrates the architecture of an Interrogator Apparatus, divided into a transmitter path (top) and a receiver path (bottom). The transmitter path starts with a circulator (3) connected to an antenna (17). The signal then passes through a receiving section (4), a mixer (15), and a demodulator section (5). The receiver path starts with an antenna (18) connected to a circulator (93), followed by a receiving section (94), a mixer (115), and a demodulator section (95). The transmitter path also includes a modulator (7) and a first spreading section (13), which are connected to a timing control section (16). The receiver path includes a modulator (97) and a second spreading section (14), which are also connected to the timing control section (16). The timing control section (16) is connected to a control section (6), which in turn is connected to a memory unit (9) and an oscillator (8). The oscillator (8) provides a reference signal to the modulator (7), the first spreading section (13), the second spreading section (14), and the mixer (115). The control section (6) also provides control signals to the modulator (97), the second spreading section (14), and the mixer (15). The memory unit (9) is connected to the control section (6) via a bidirectional interface (9).

INTERROGATOR APPARATUS

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FIG. 22

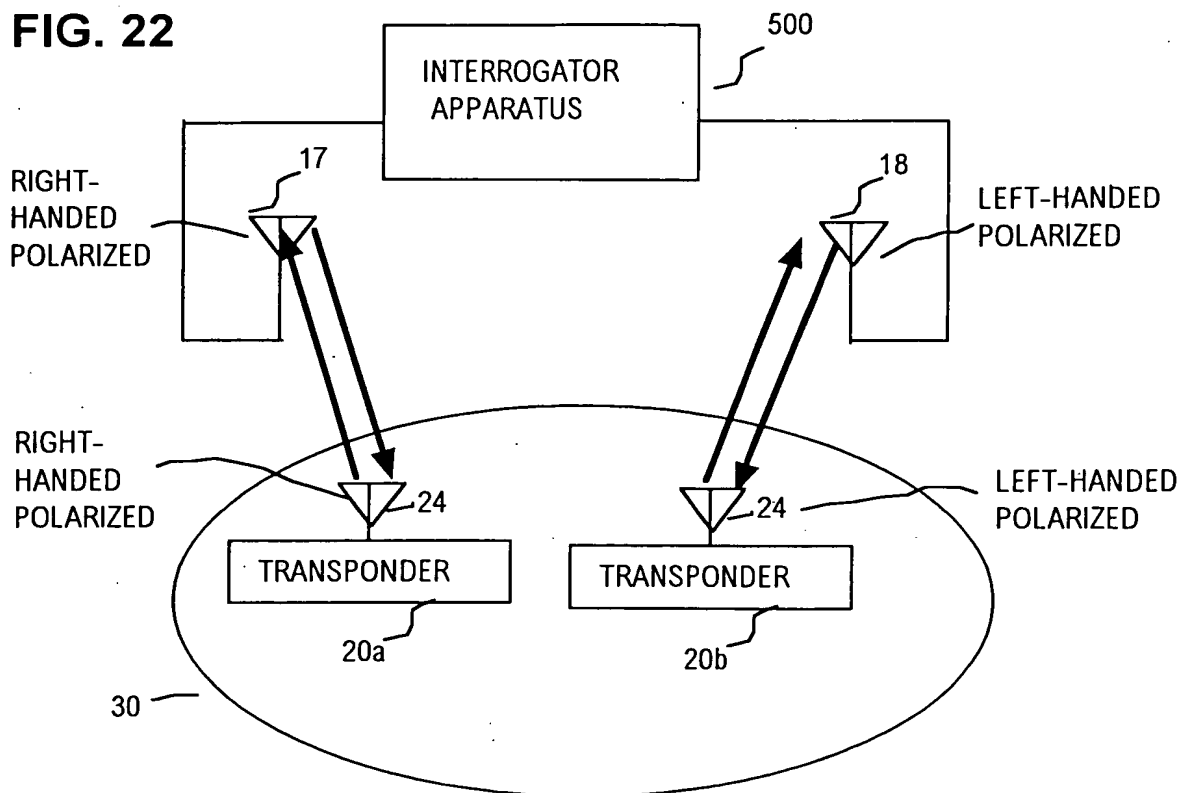
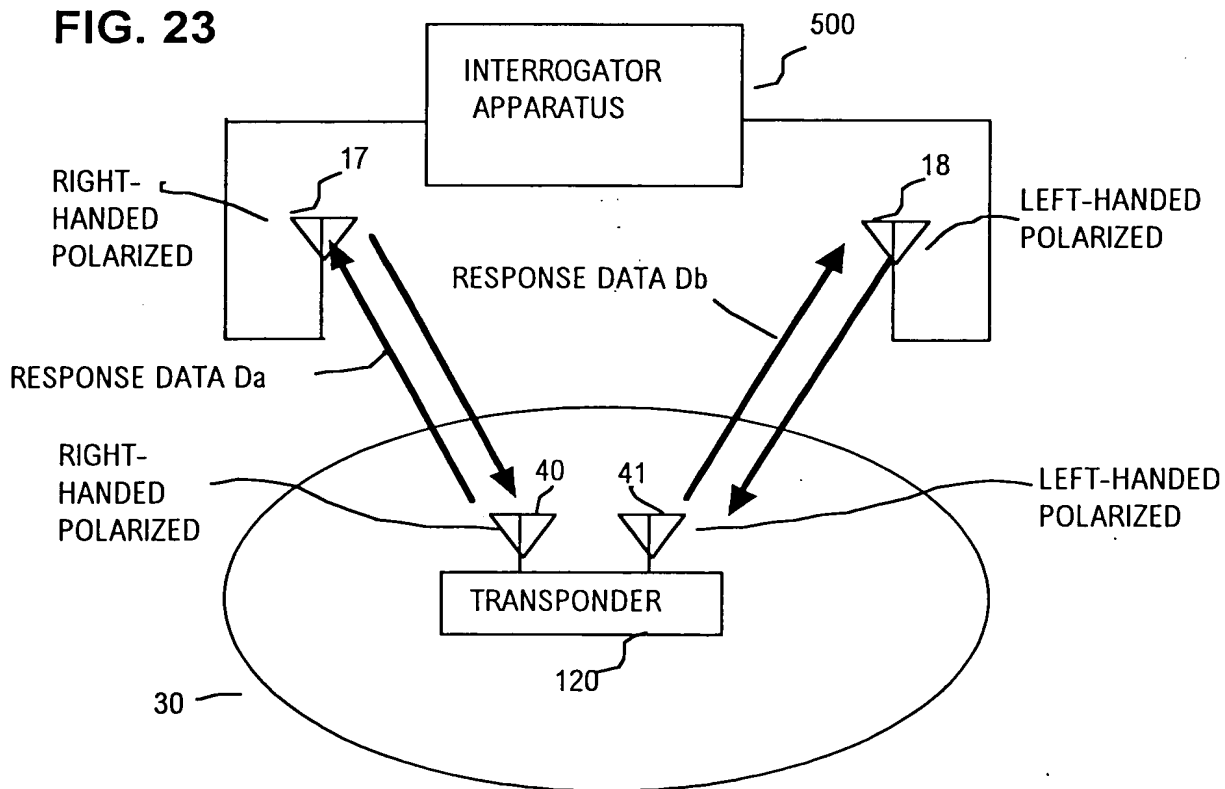


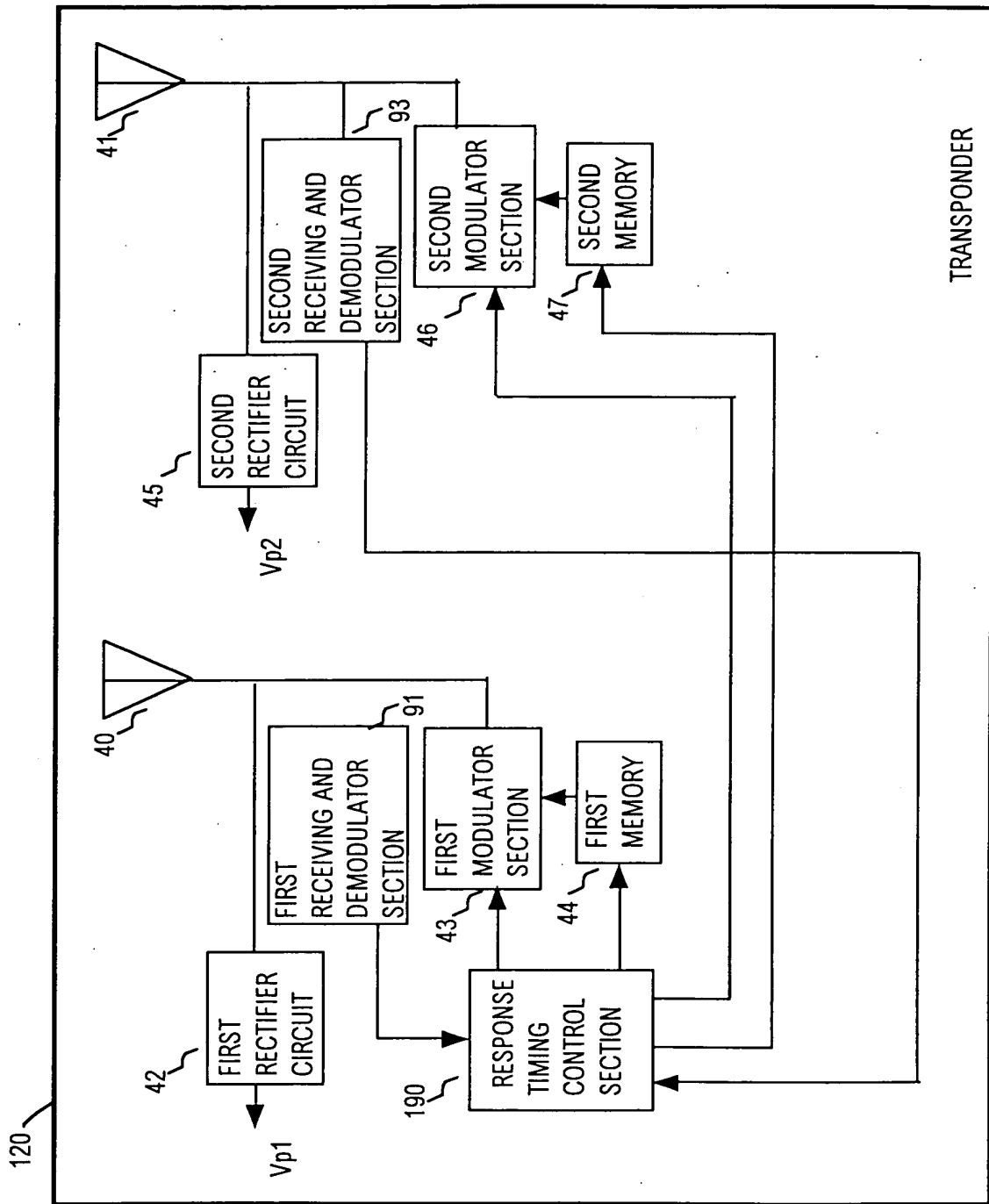
FIG. 23



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FIG. 24



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FIG. 25

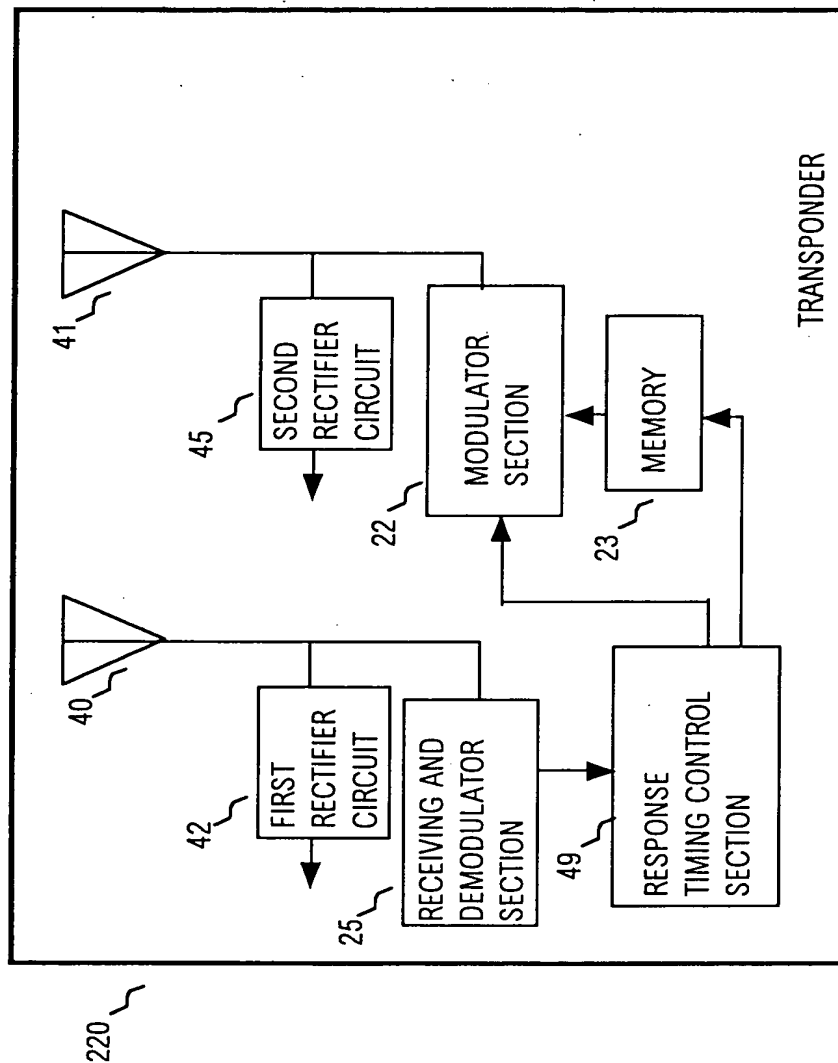


FIG. 26

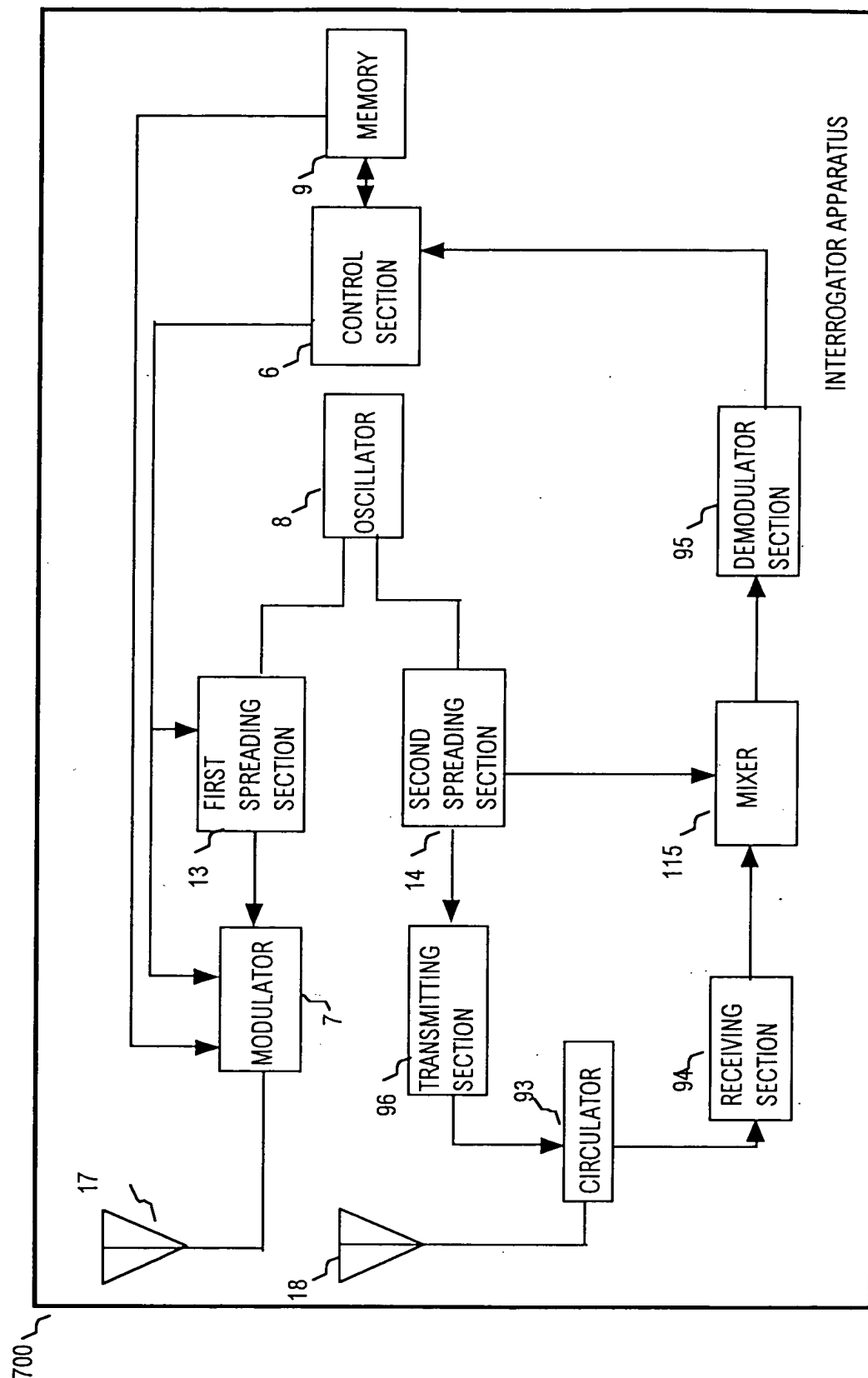


FIG. 27

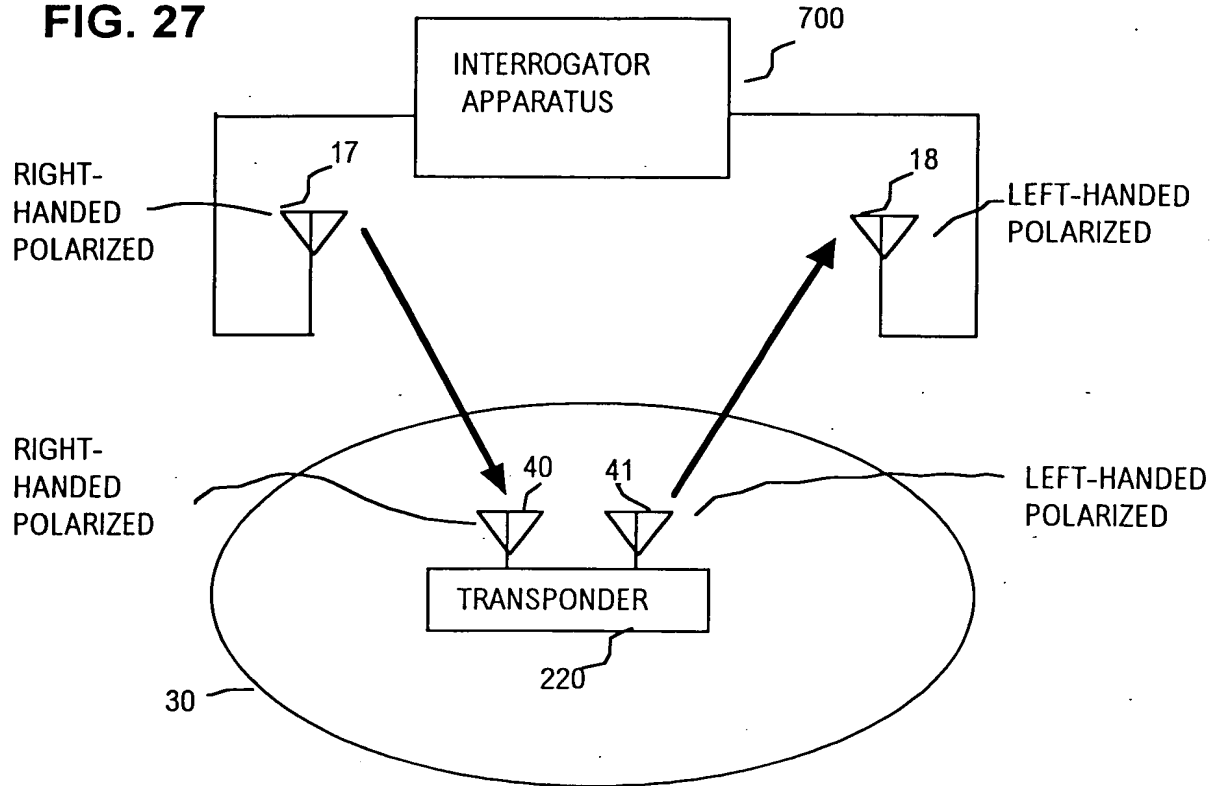


FIG. 28

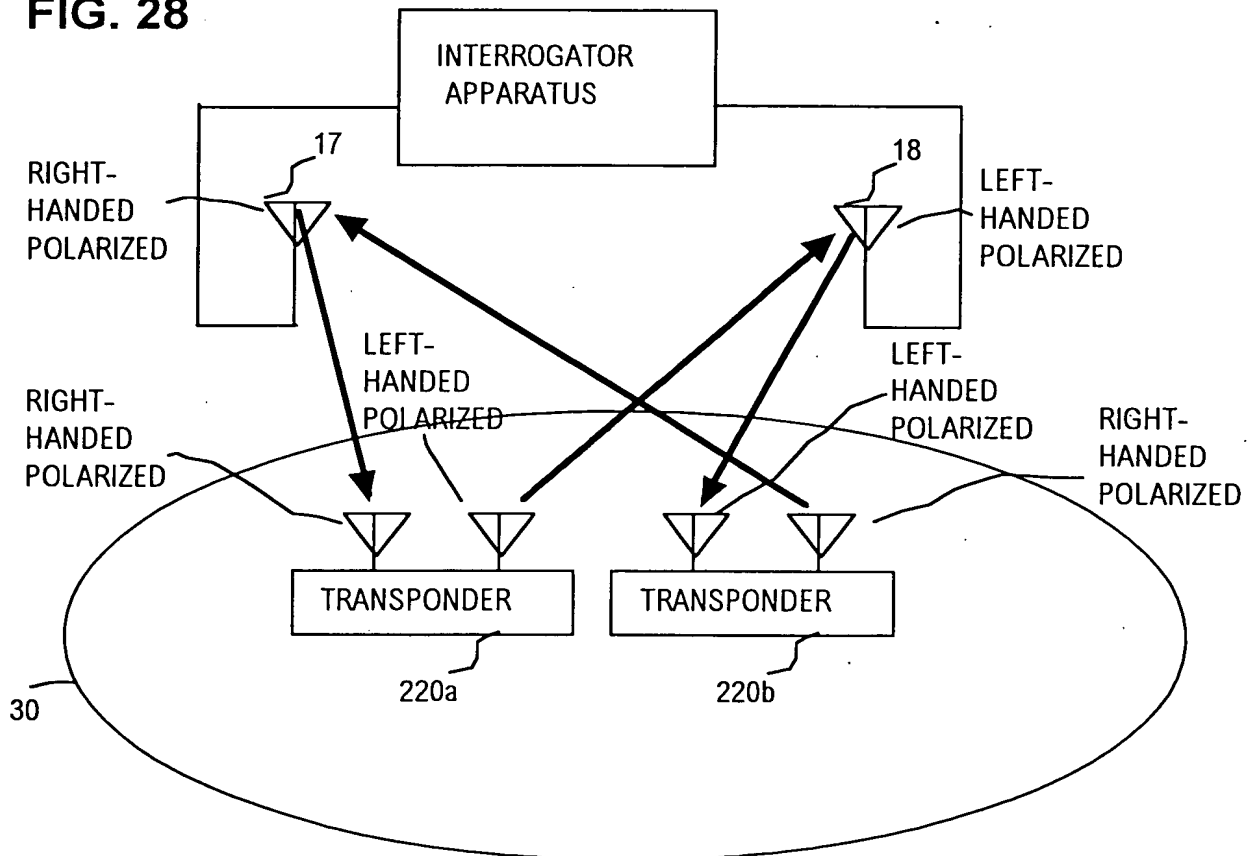


FIG. 29

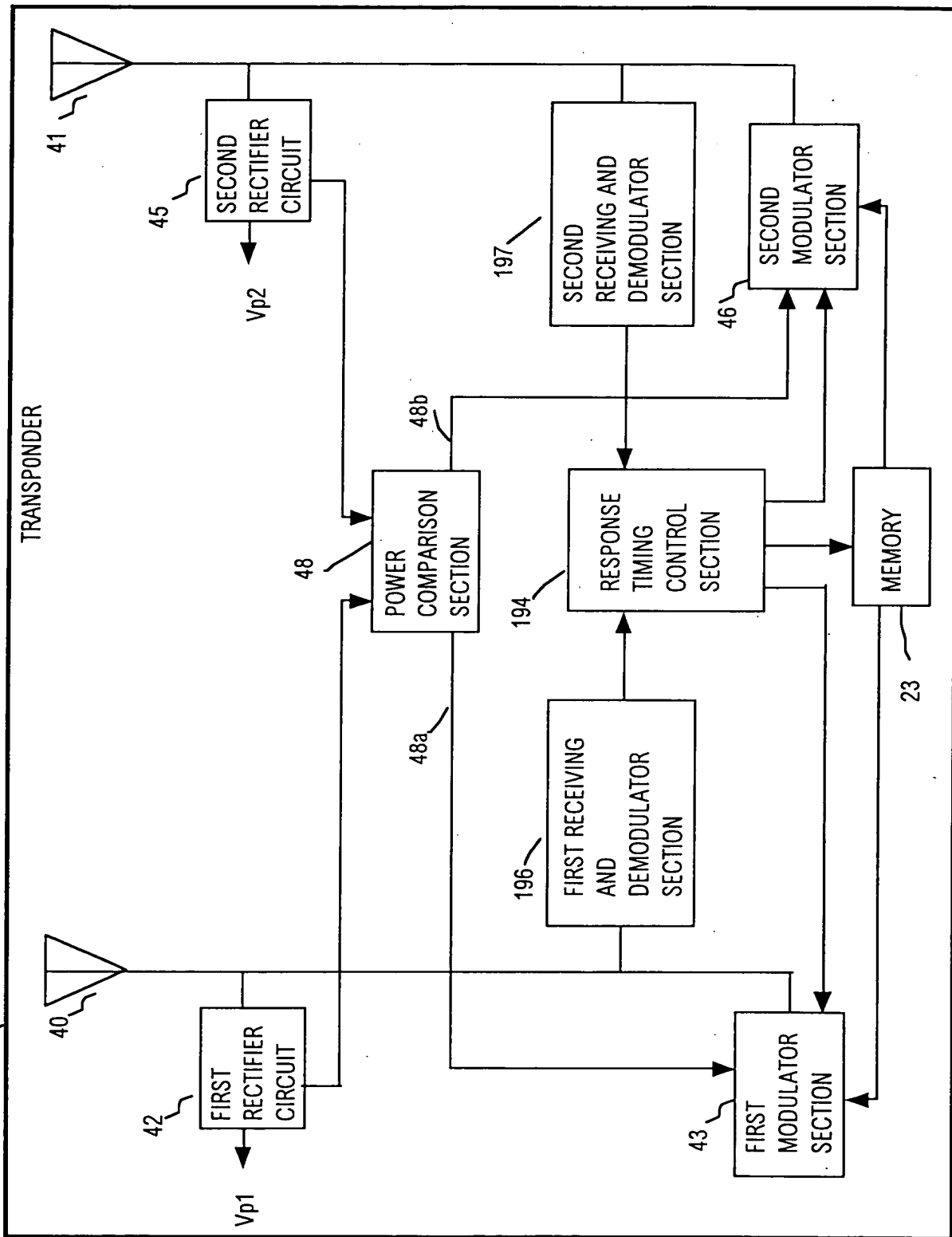


FIG. 30A

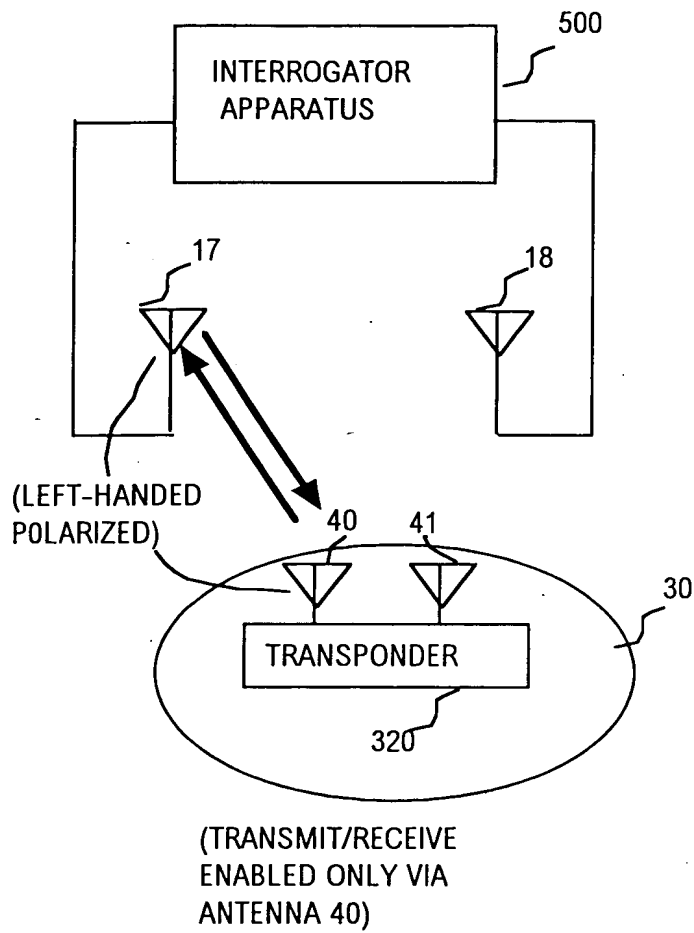
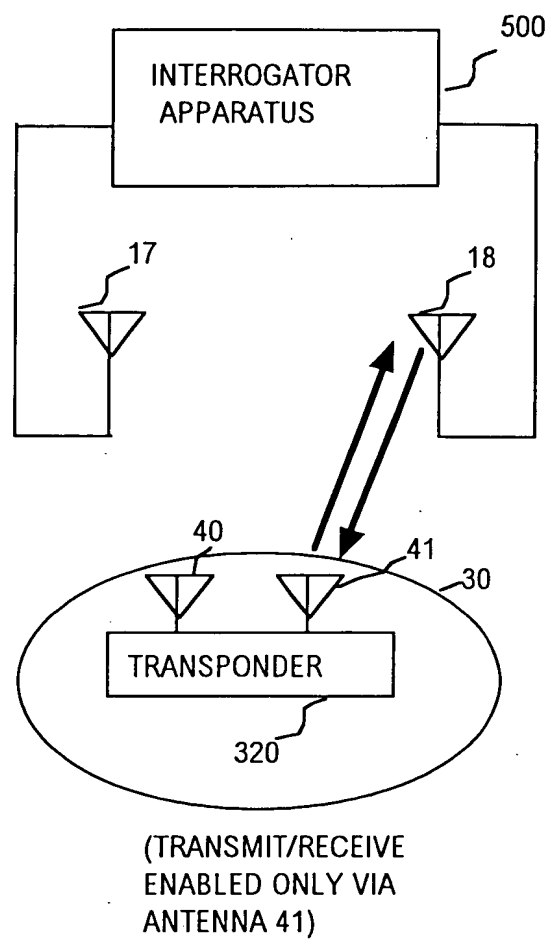
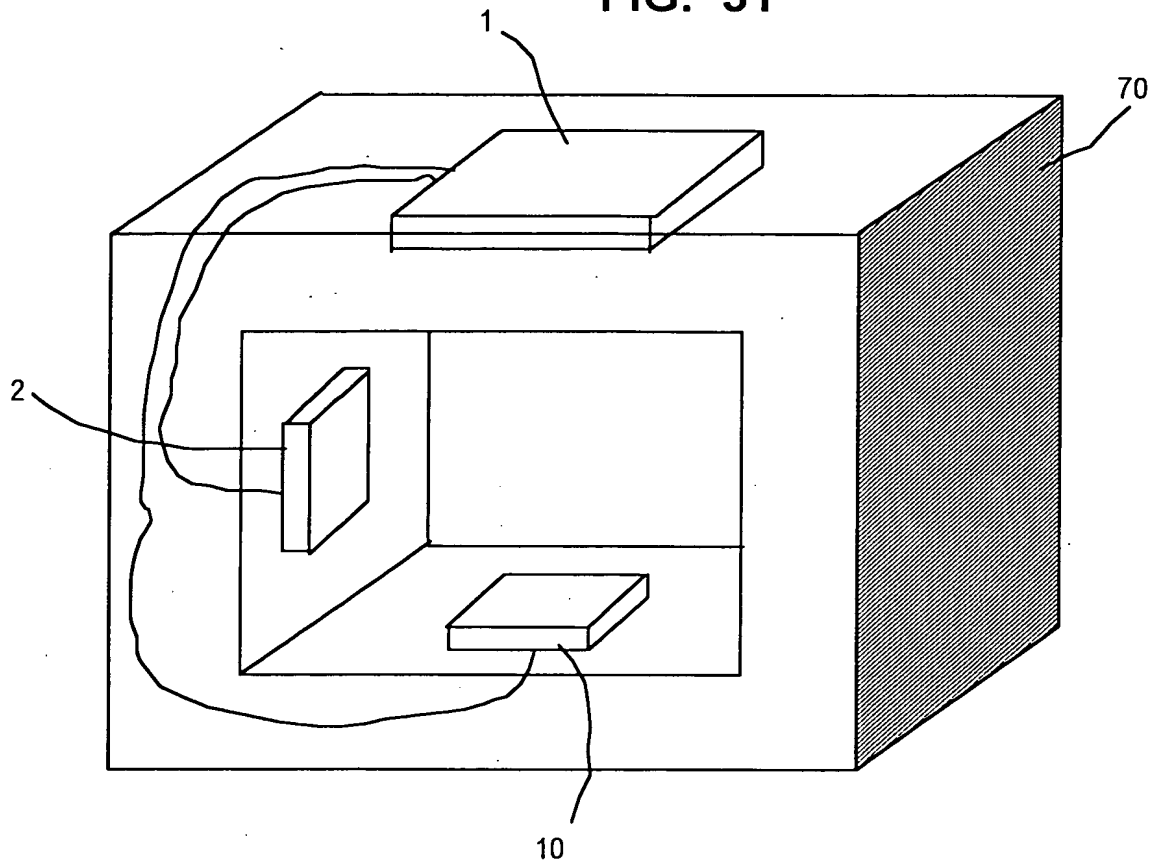


FIG. 30B



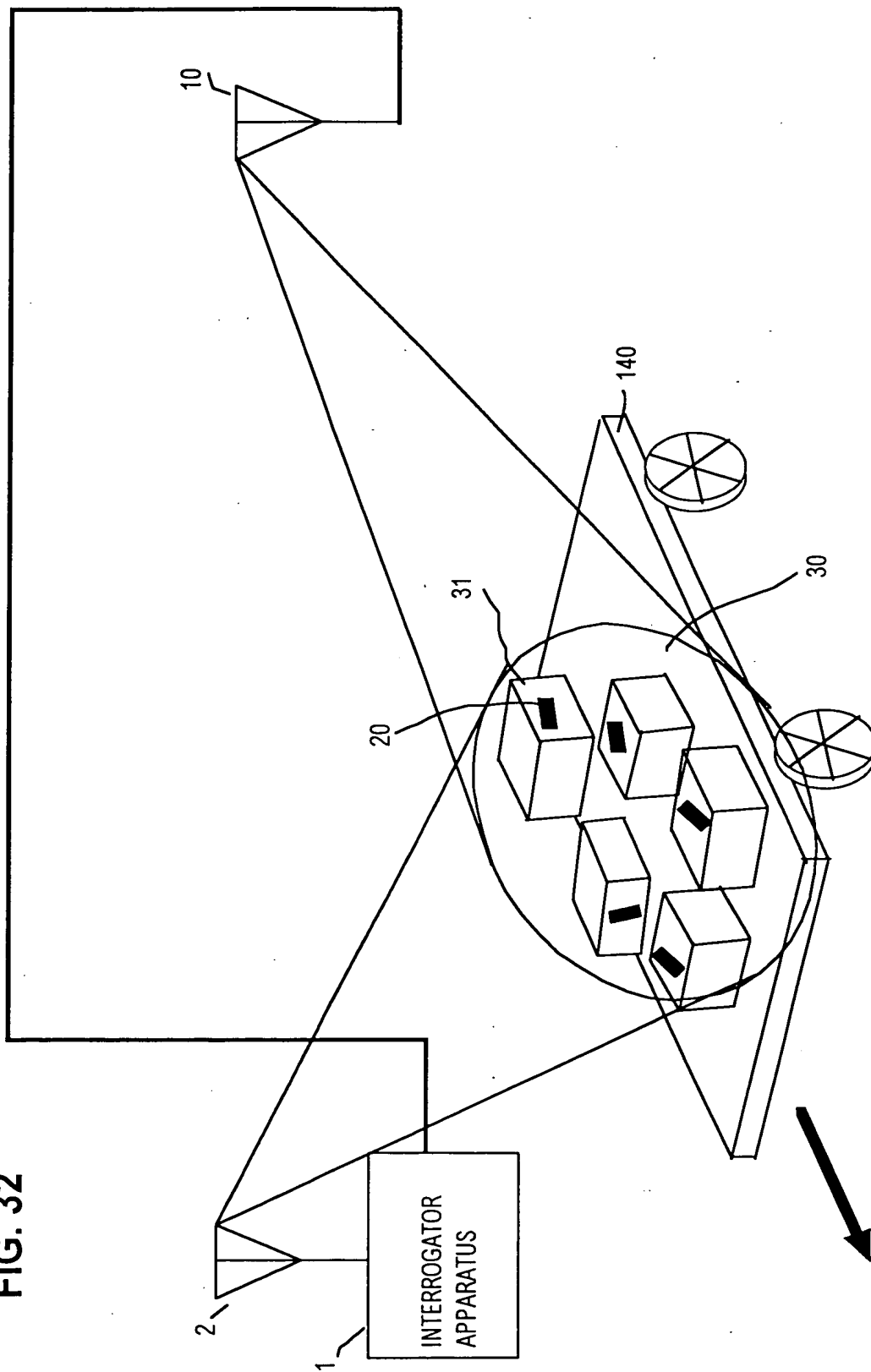
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FIG. 31



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FIG. 32



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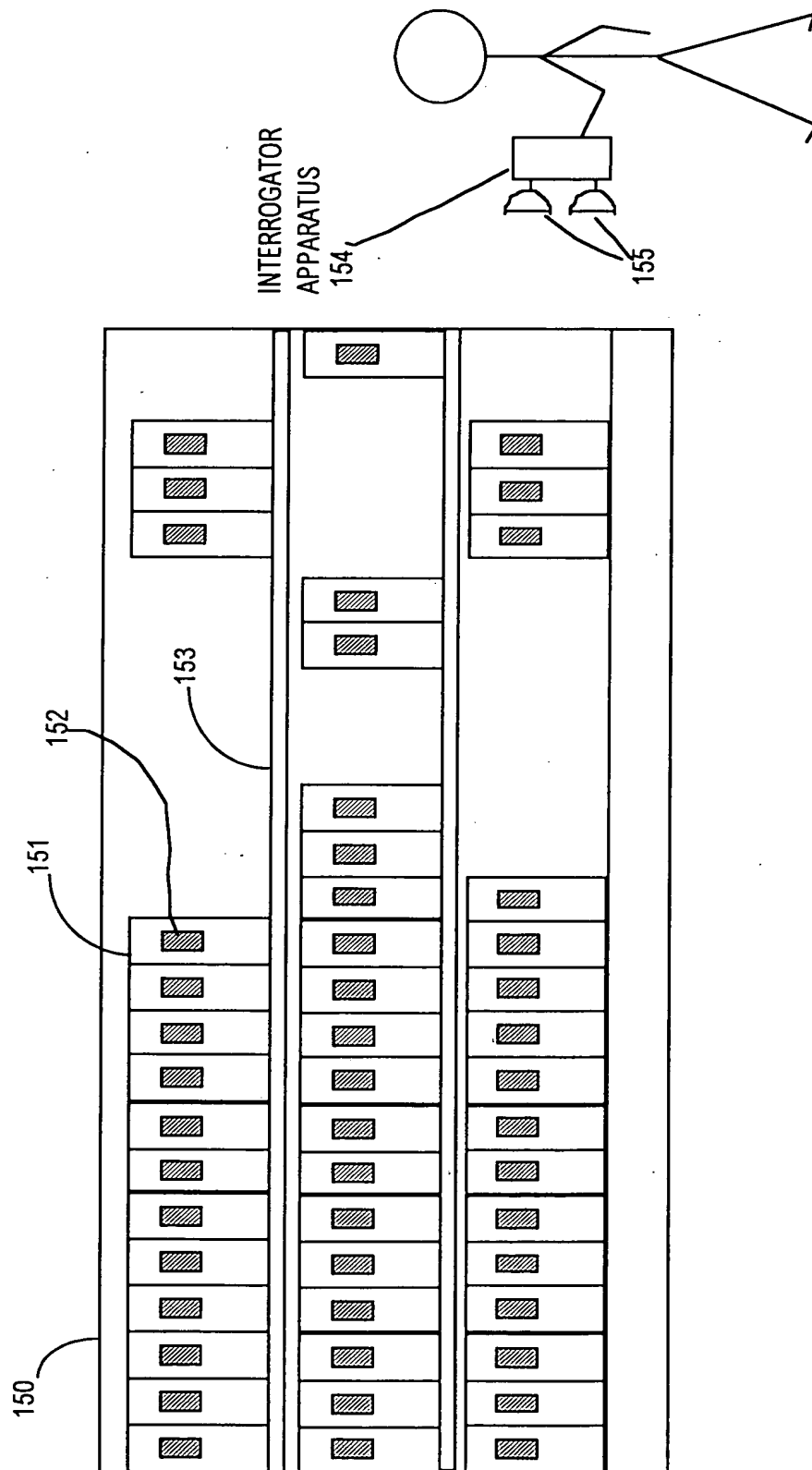
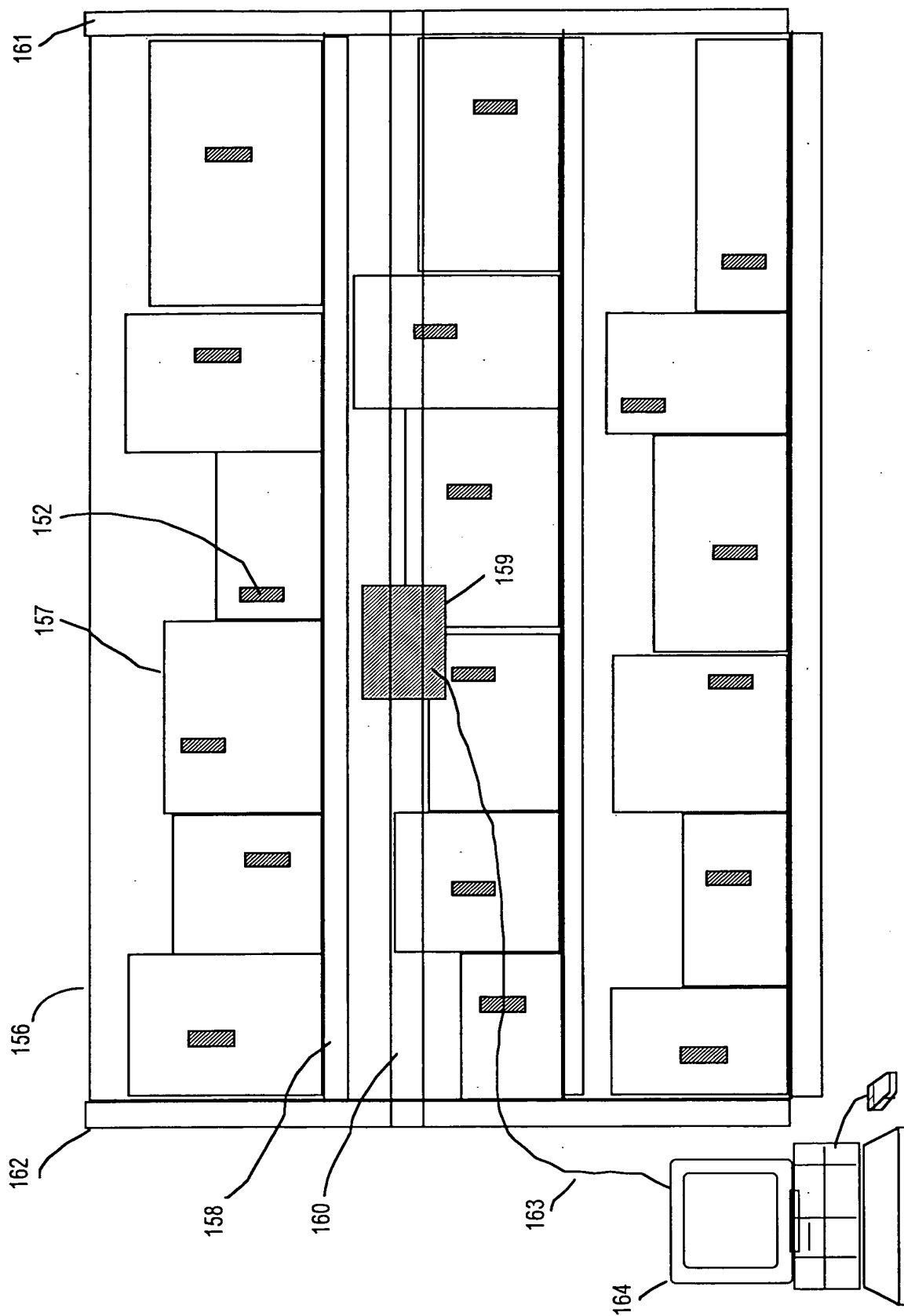
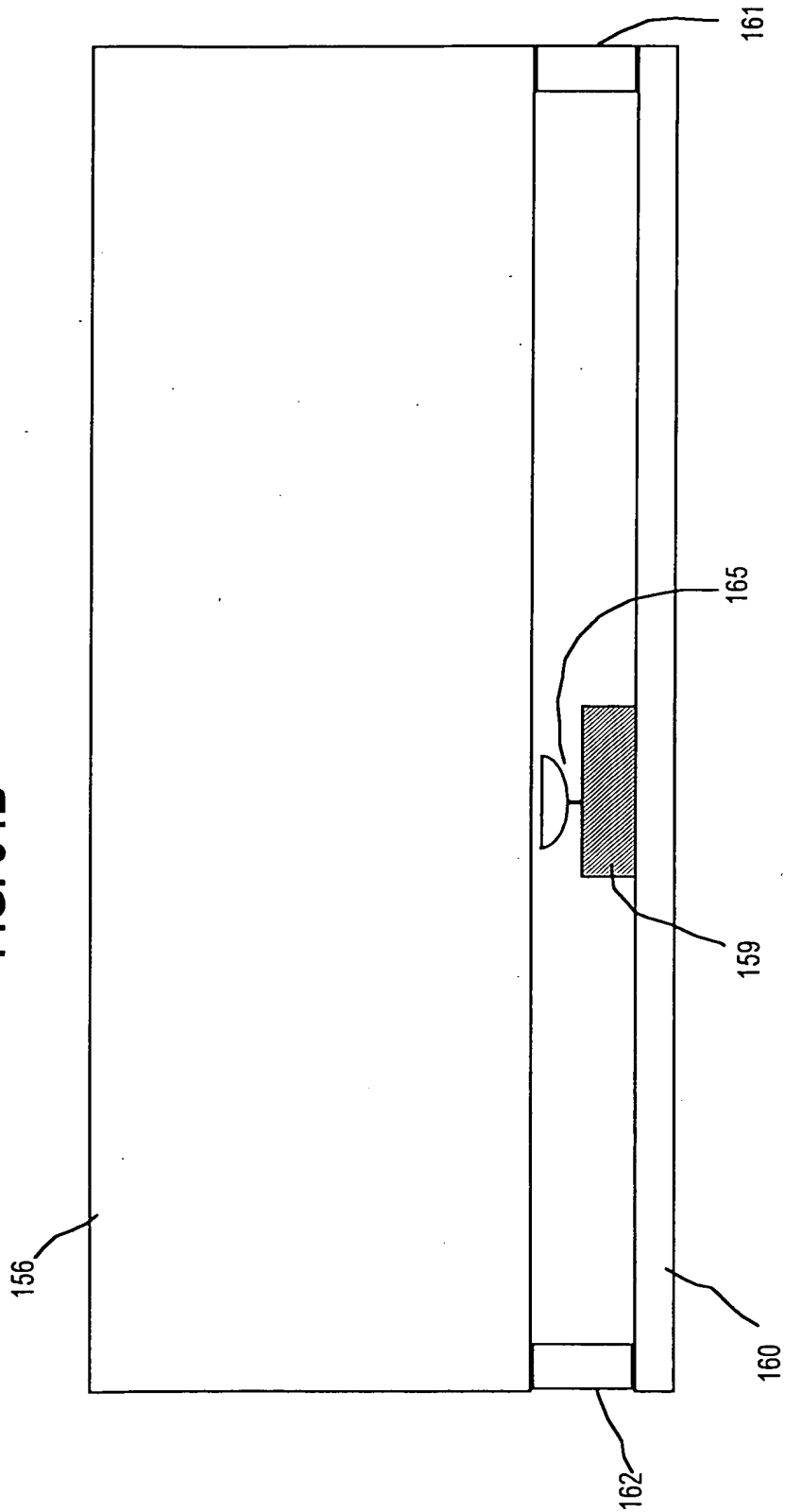


FIG. 34A



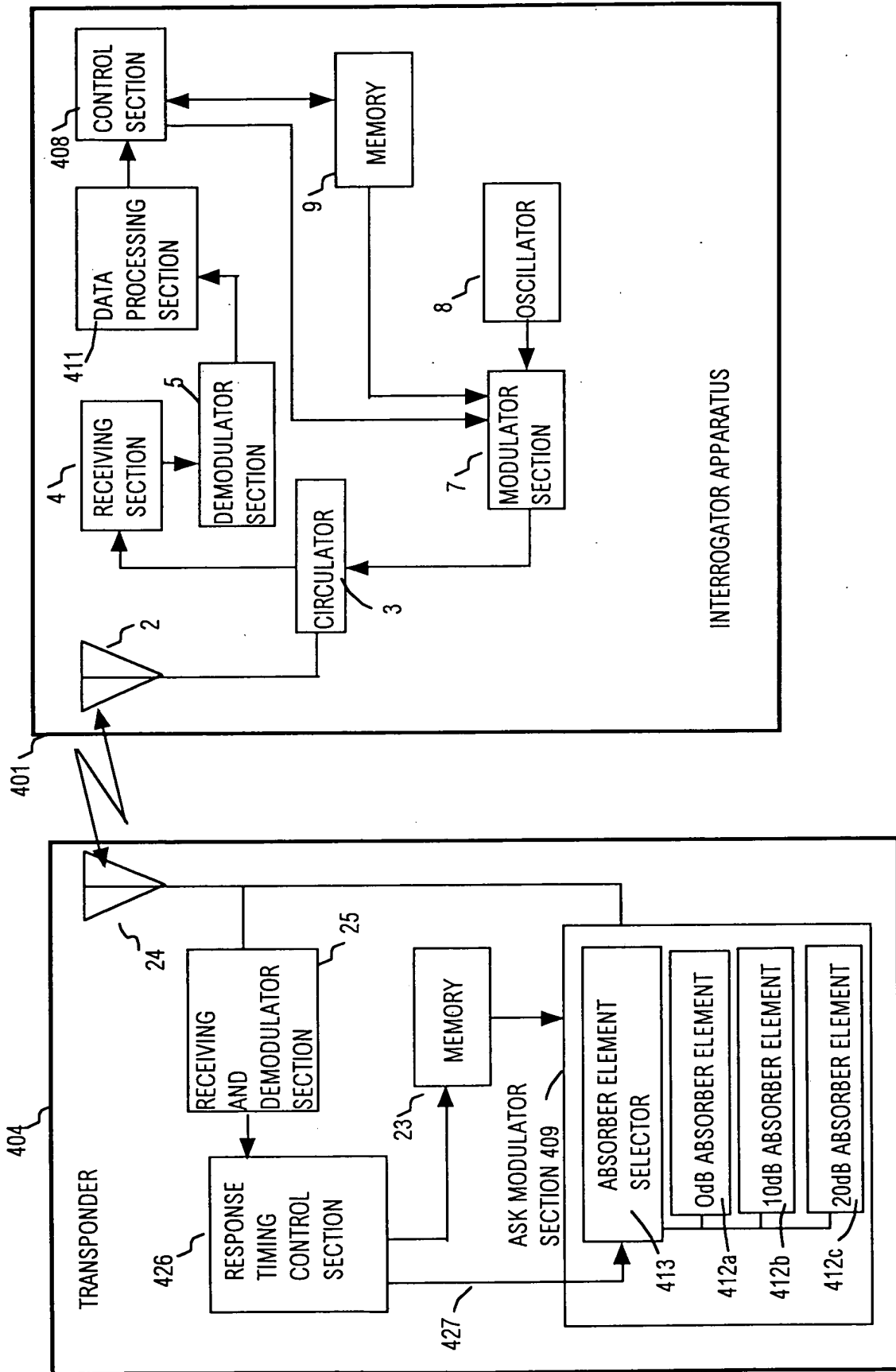
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FIG. 34B



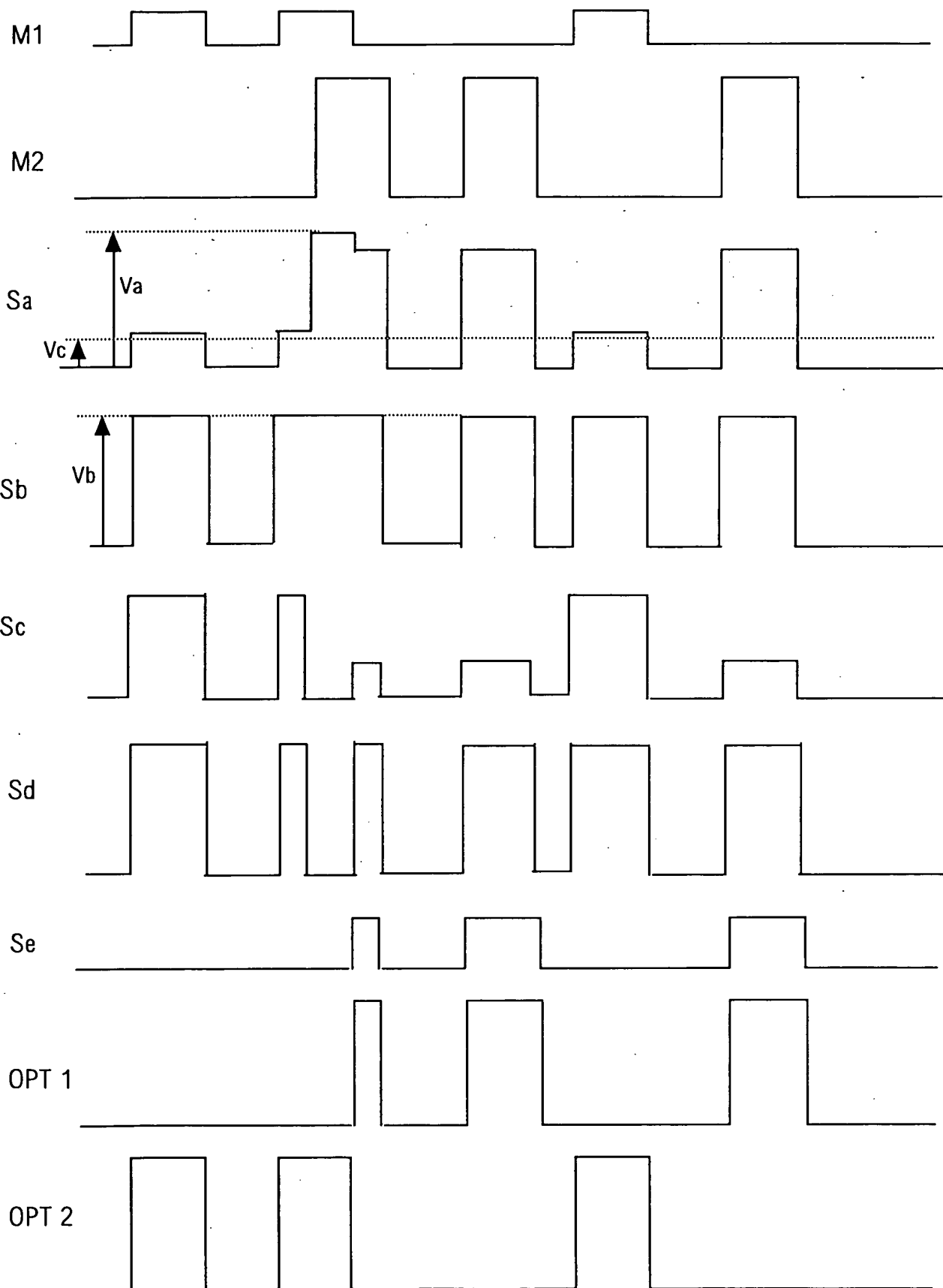
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FIG. 35



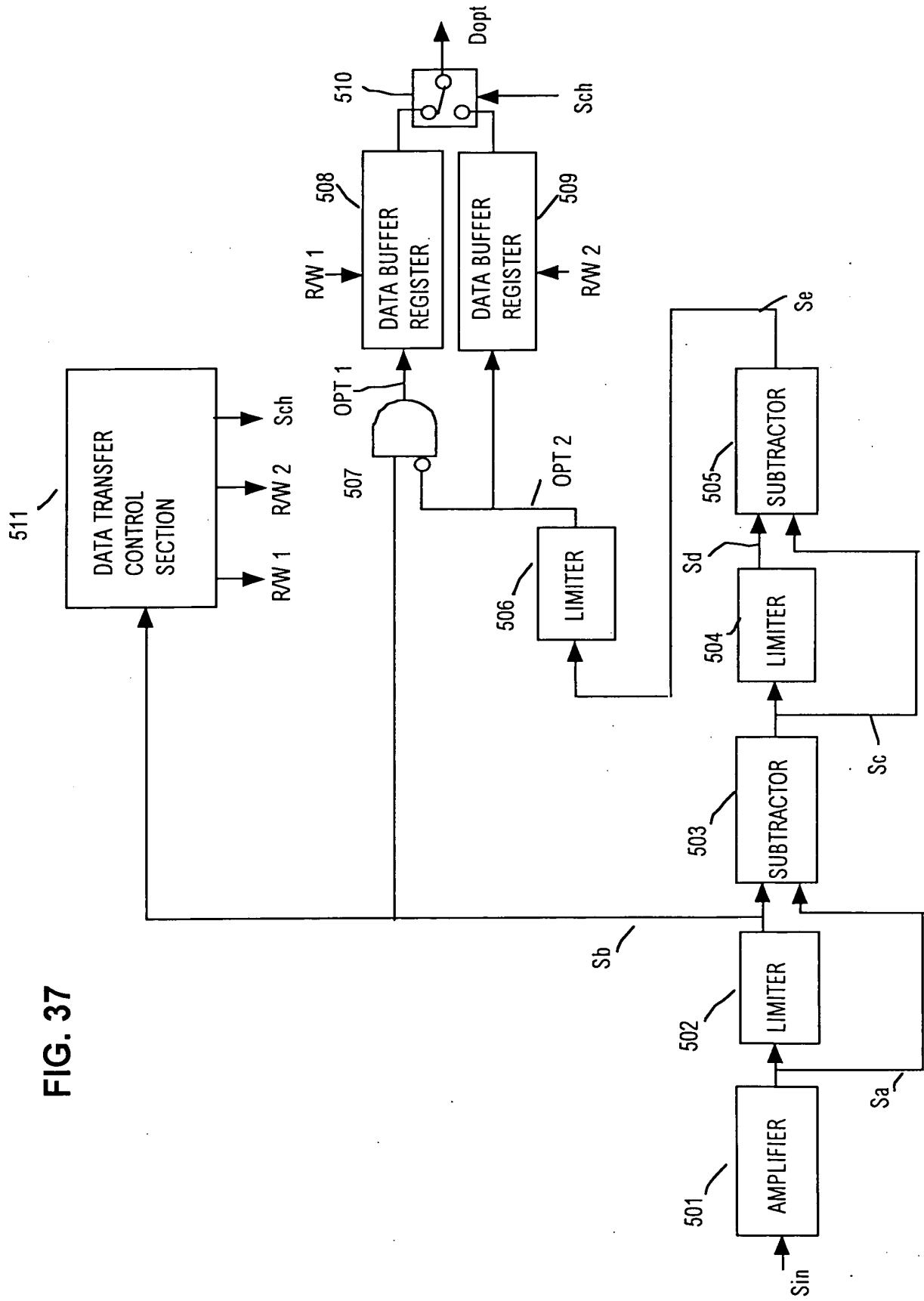
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FIG. 36



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FIG. 37



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FIG. 38

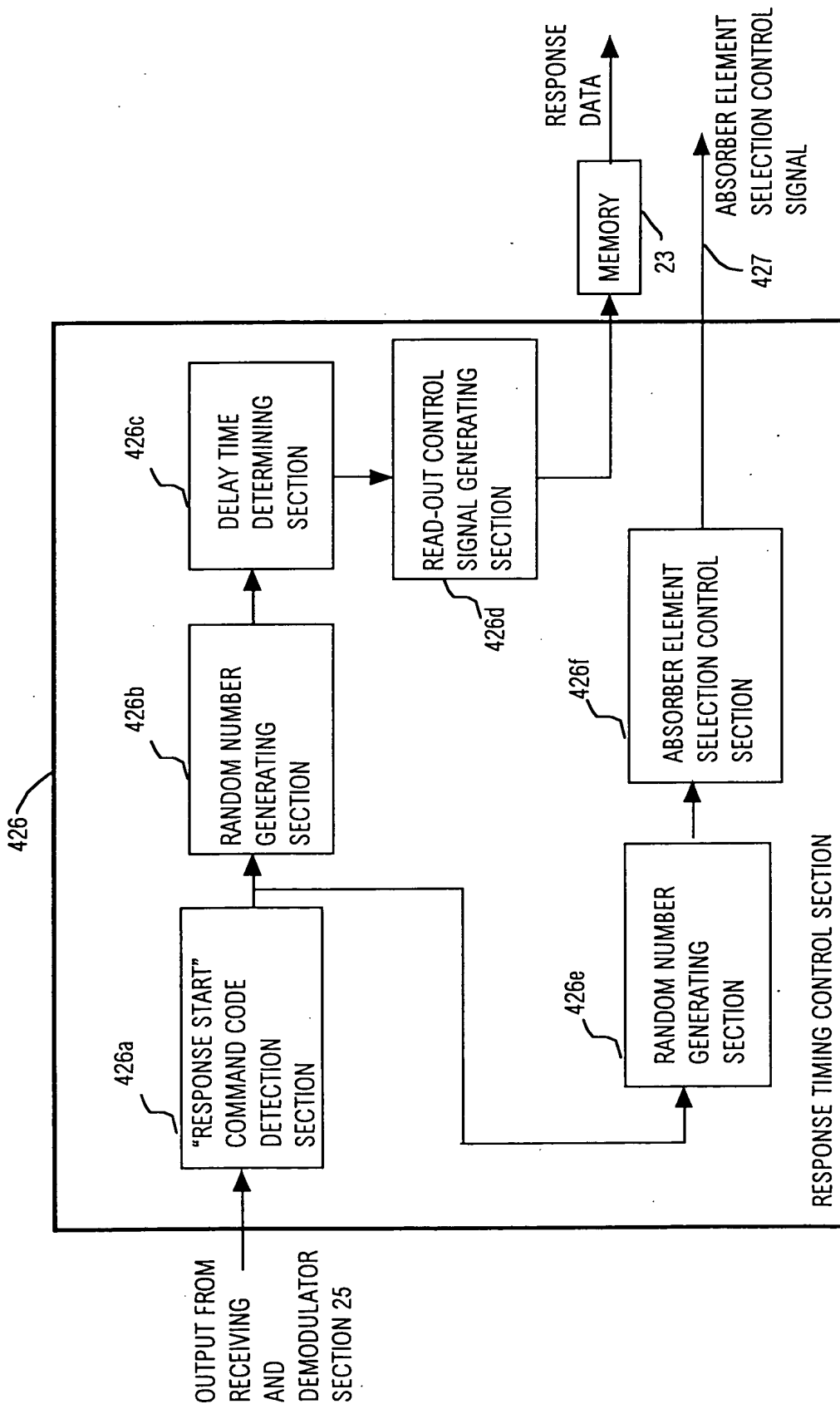
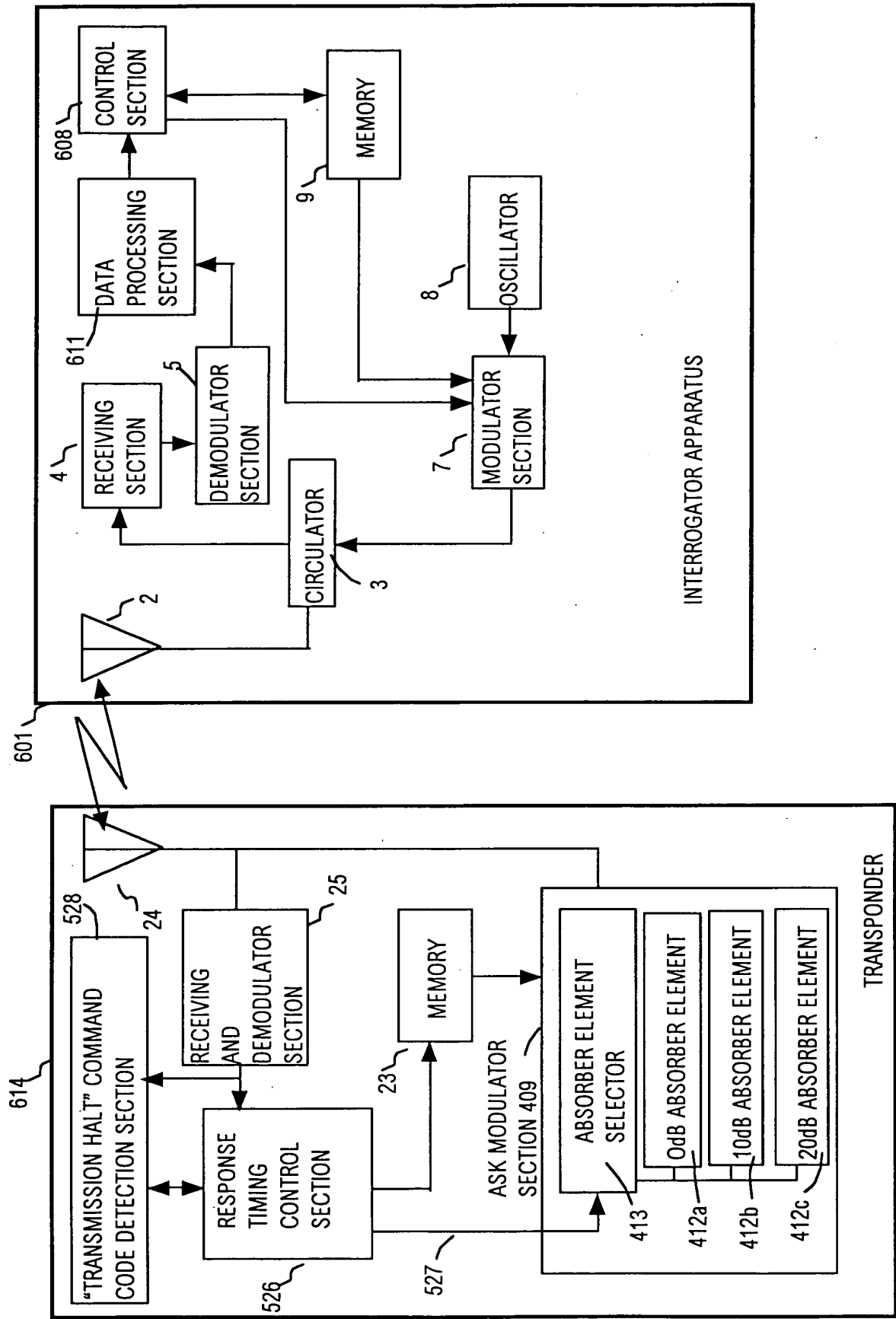


FIG. 39



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FIG. 40

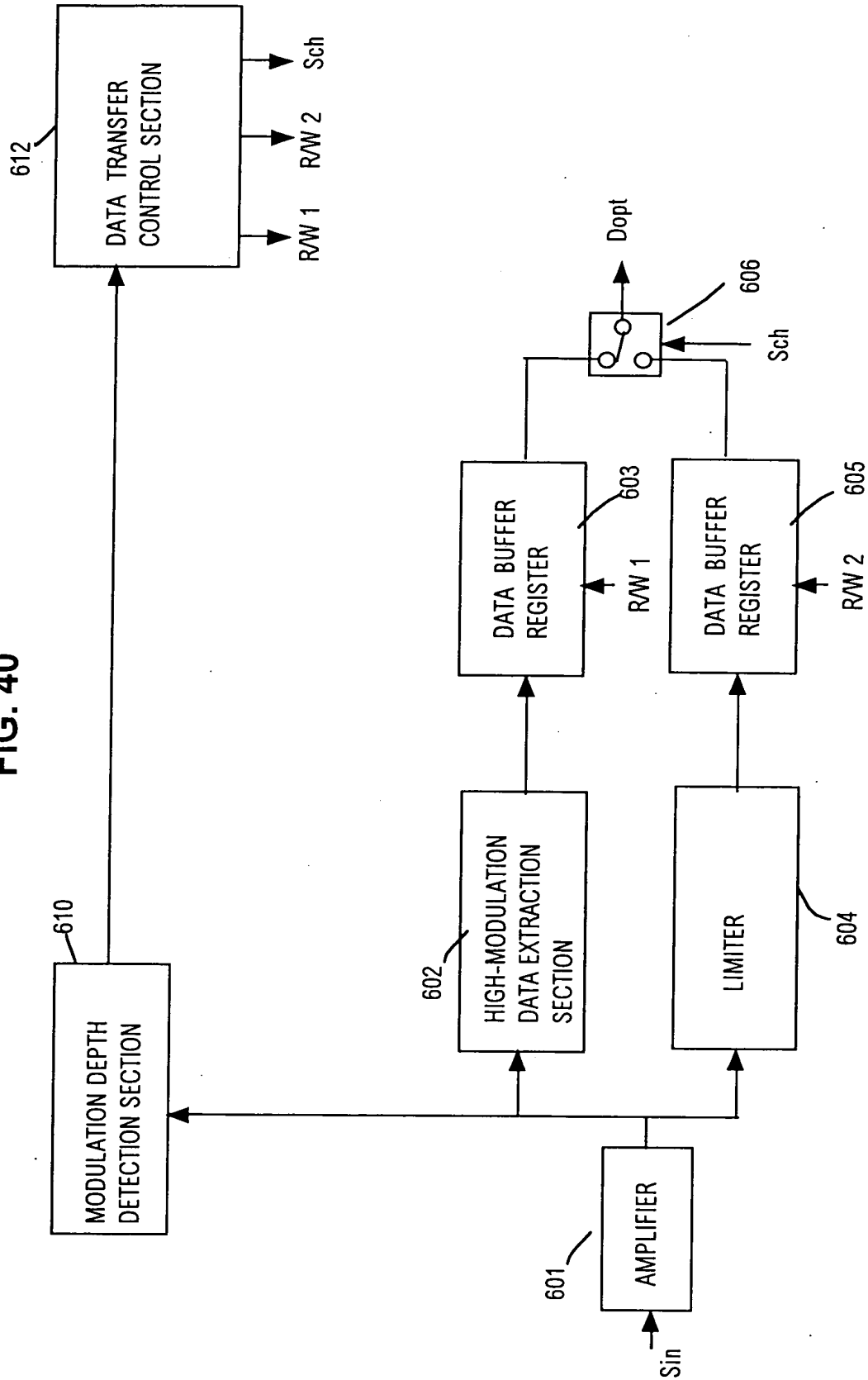


FIG. 41

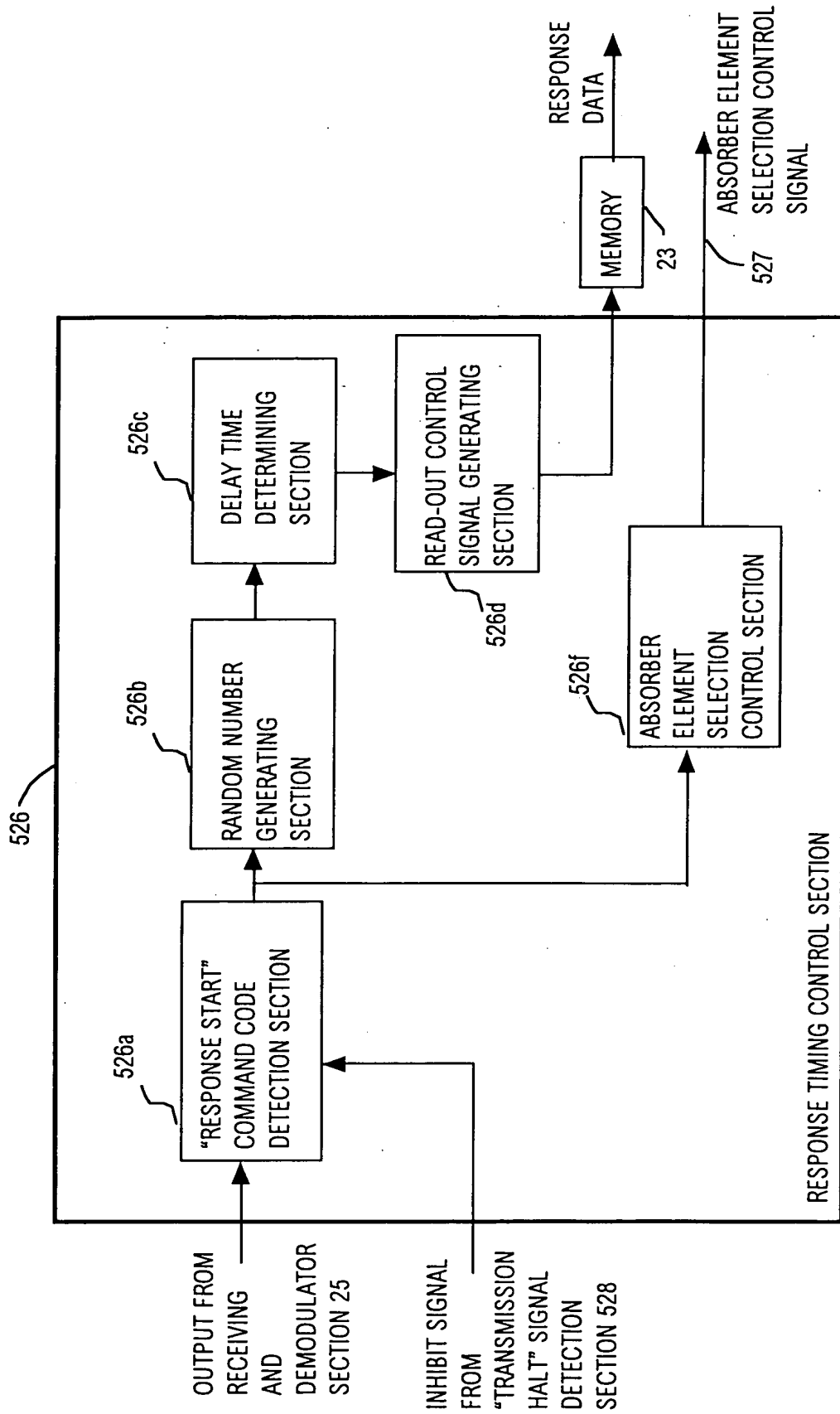


FIG. 42

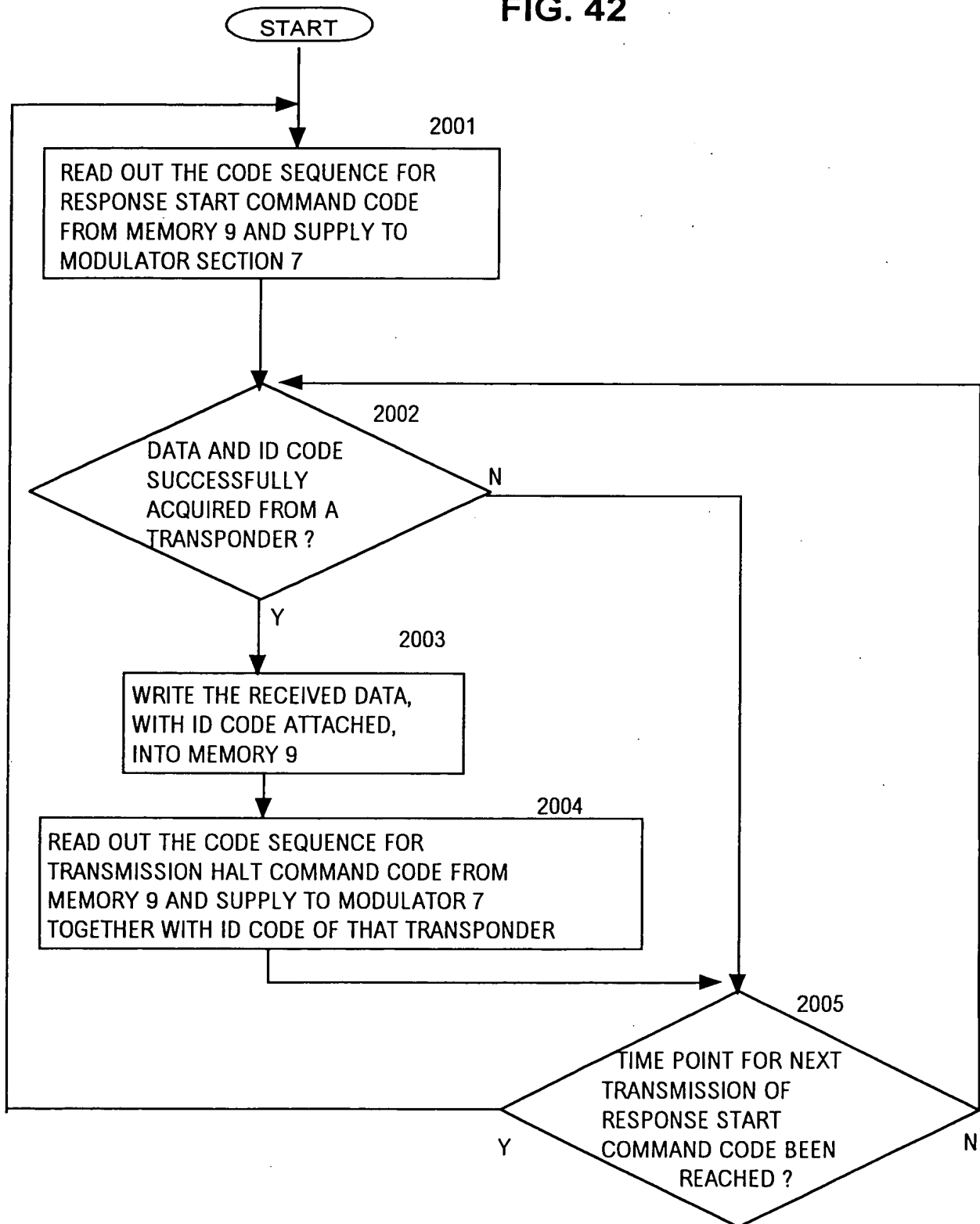


FIG. 43

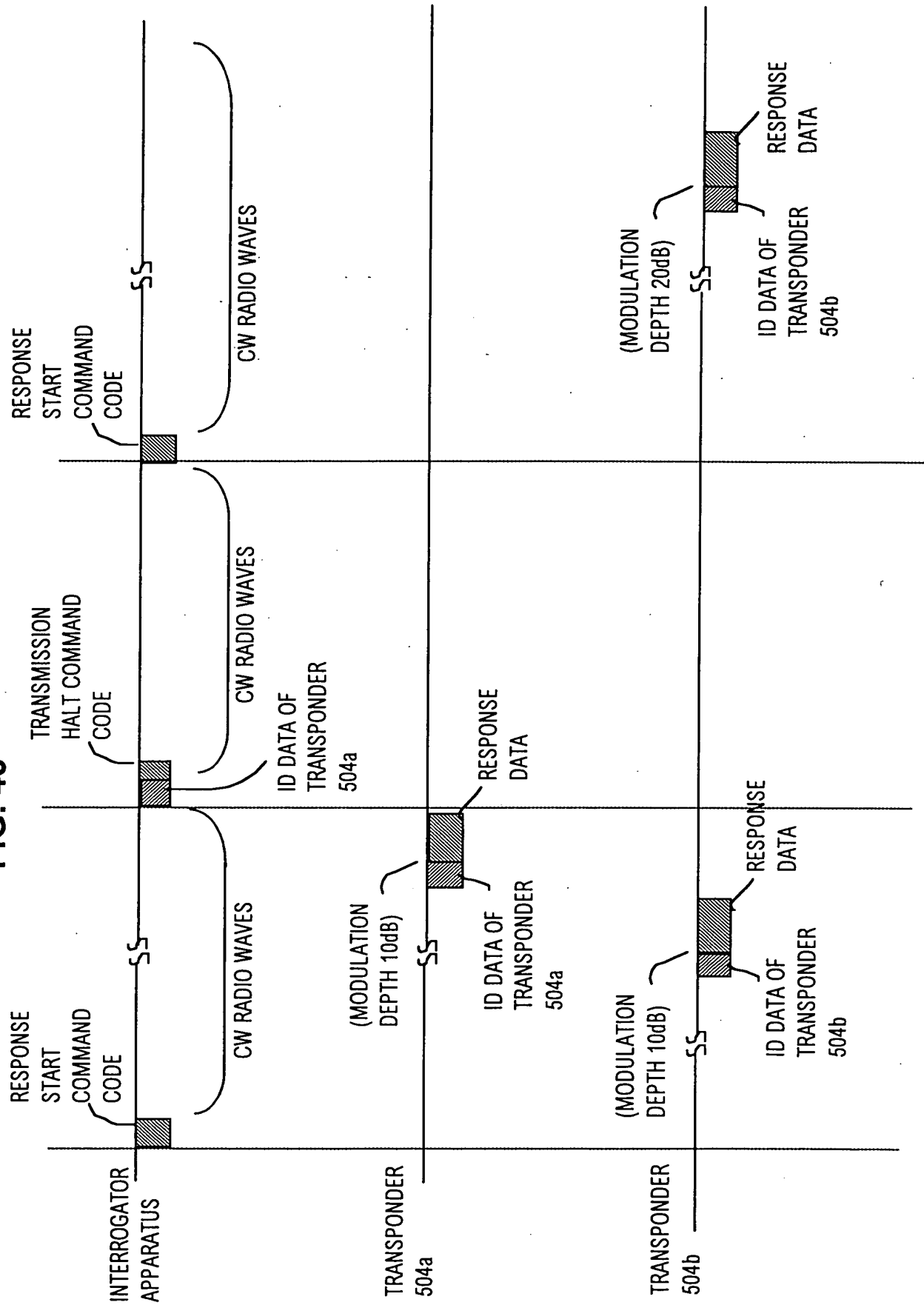


FIG. 44

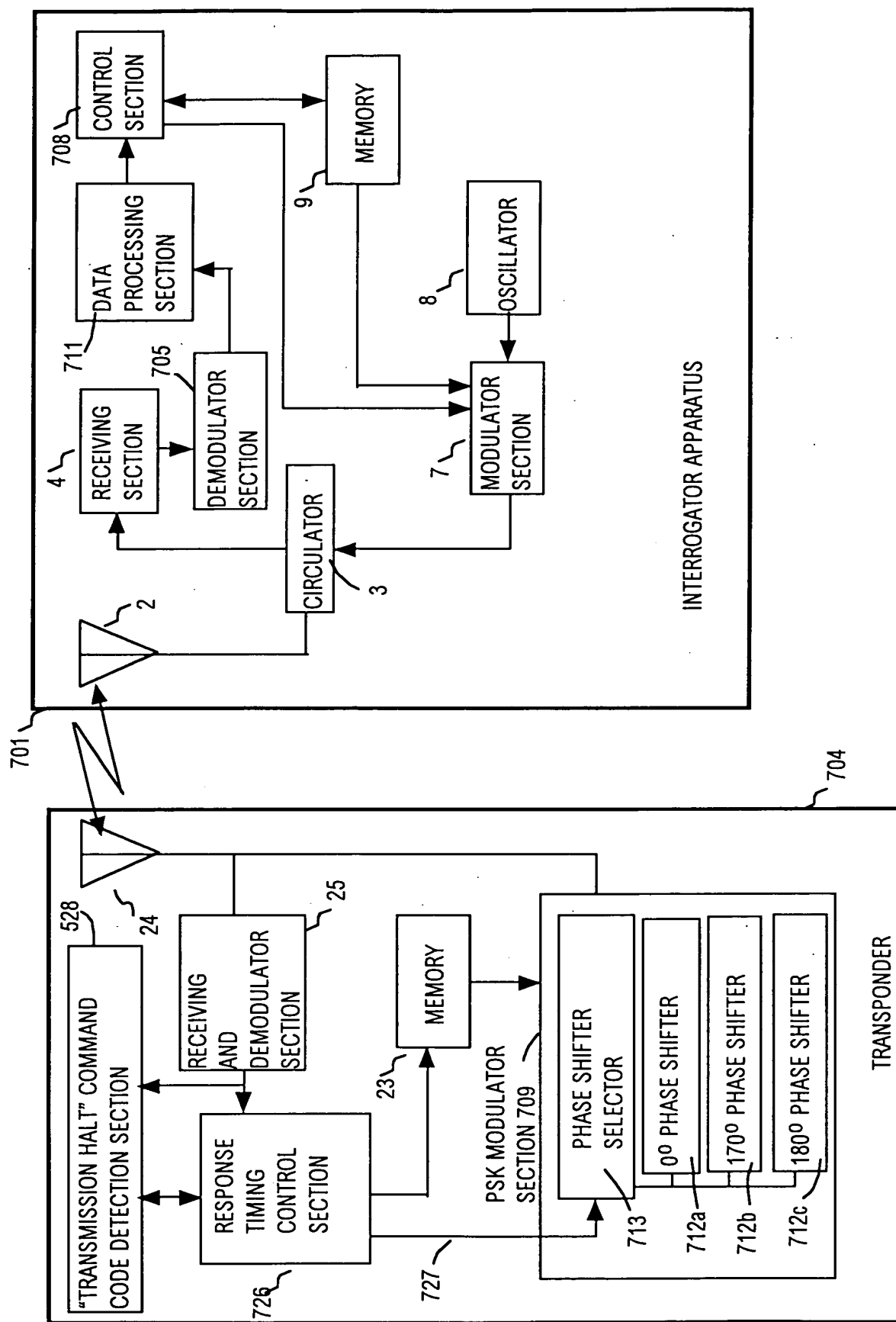
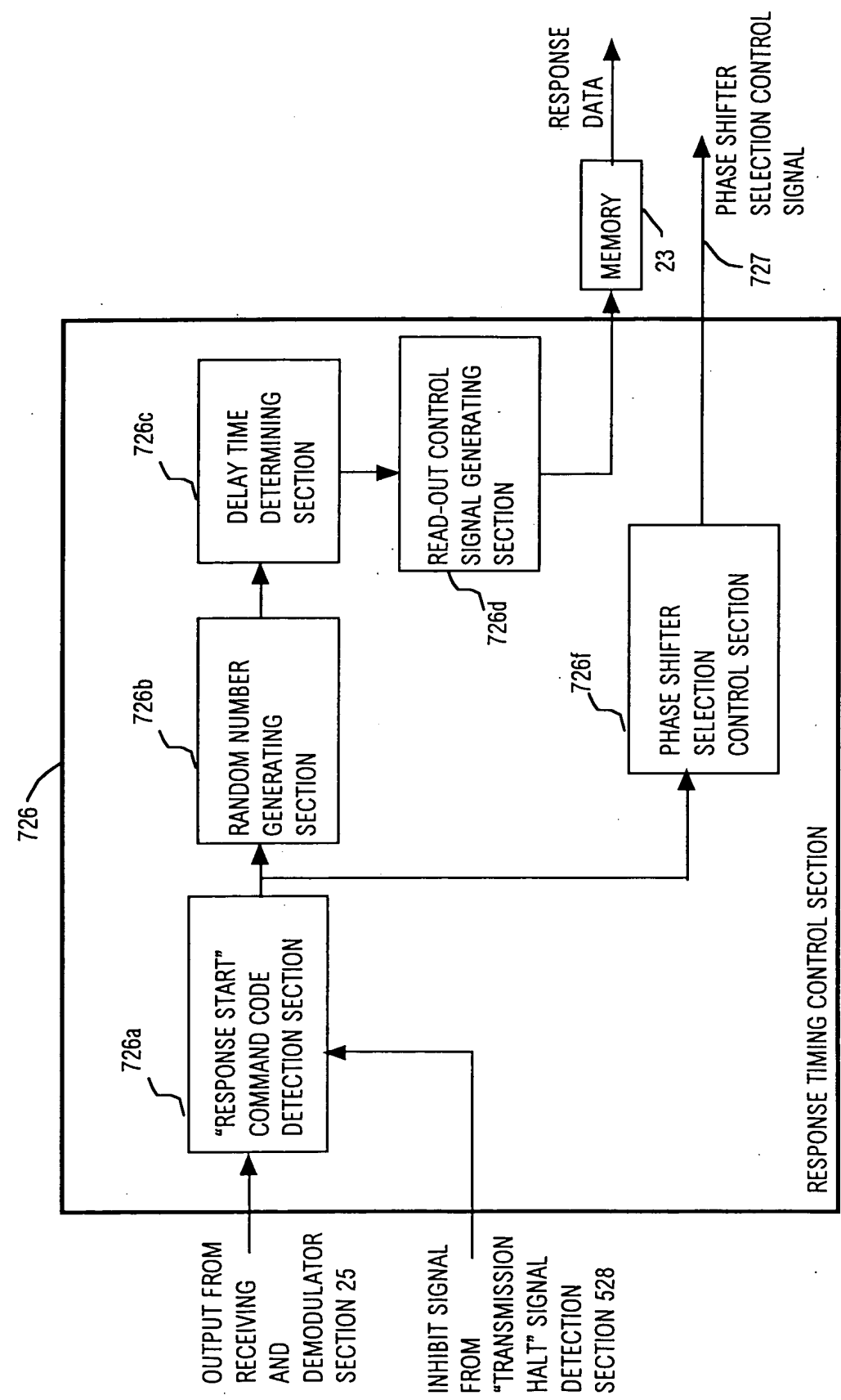


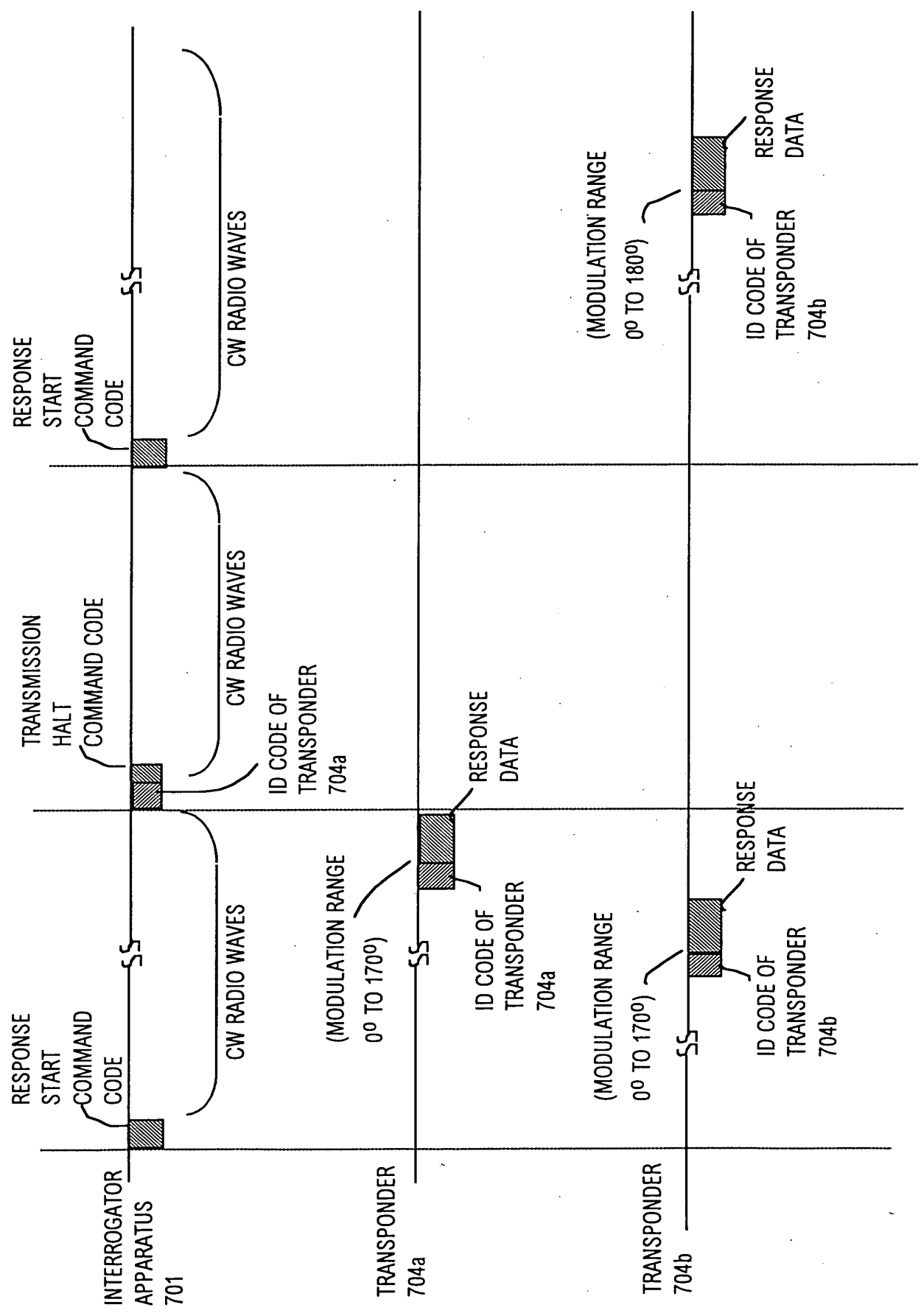
FIG. 45



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FIG. 46



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FIG. 47

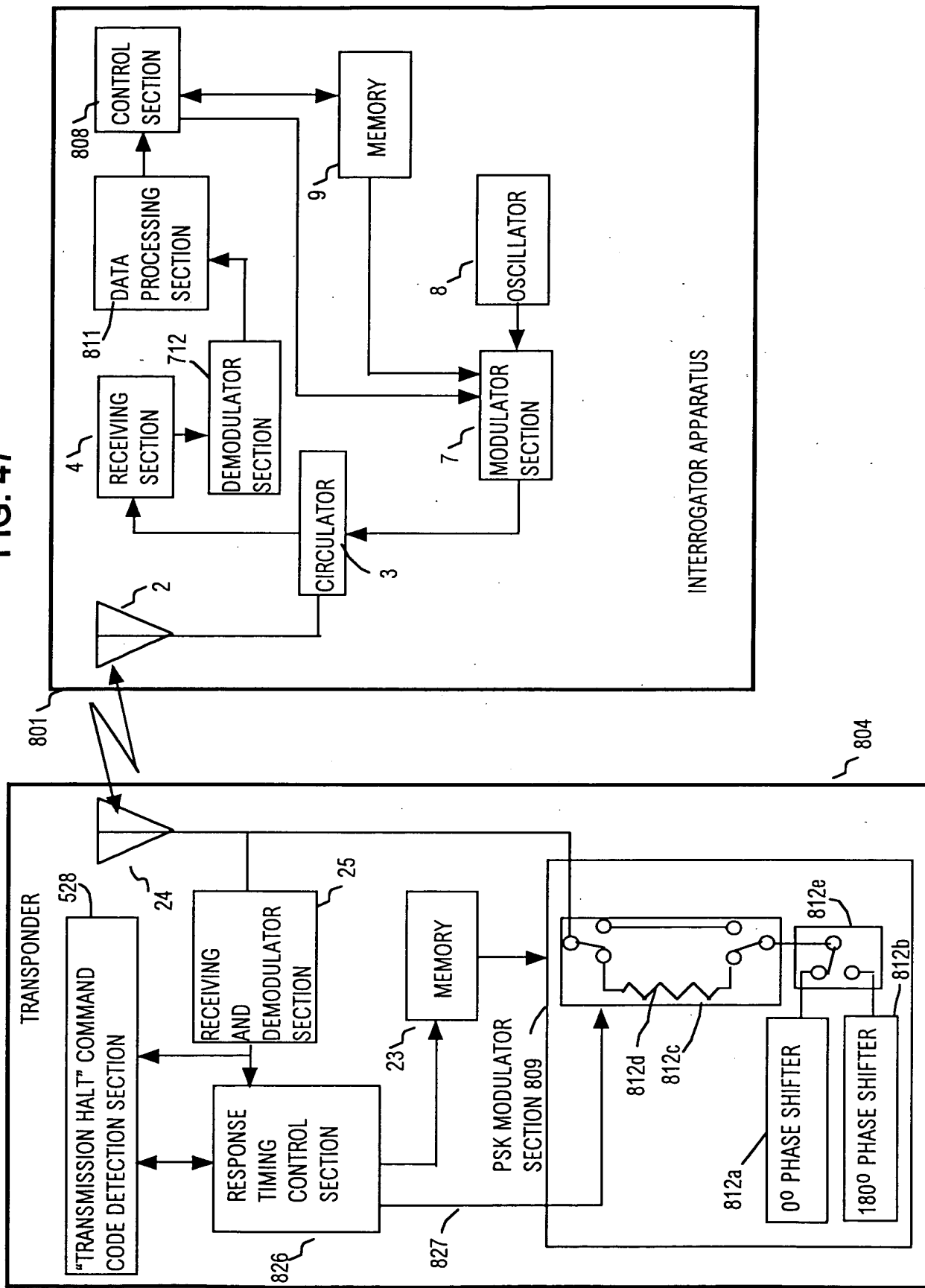
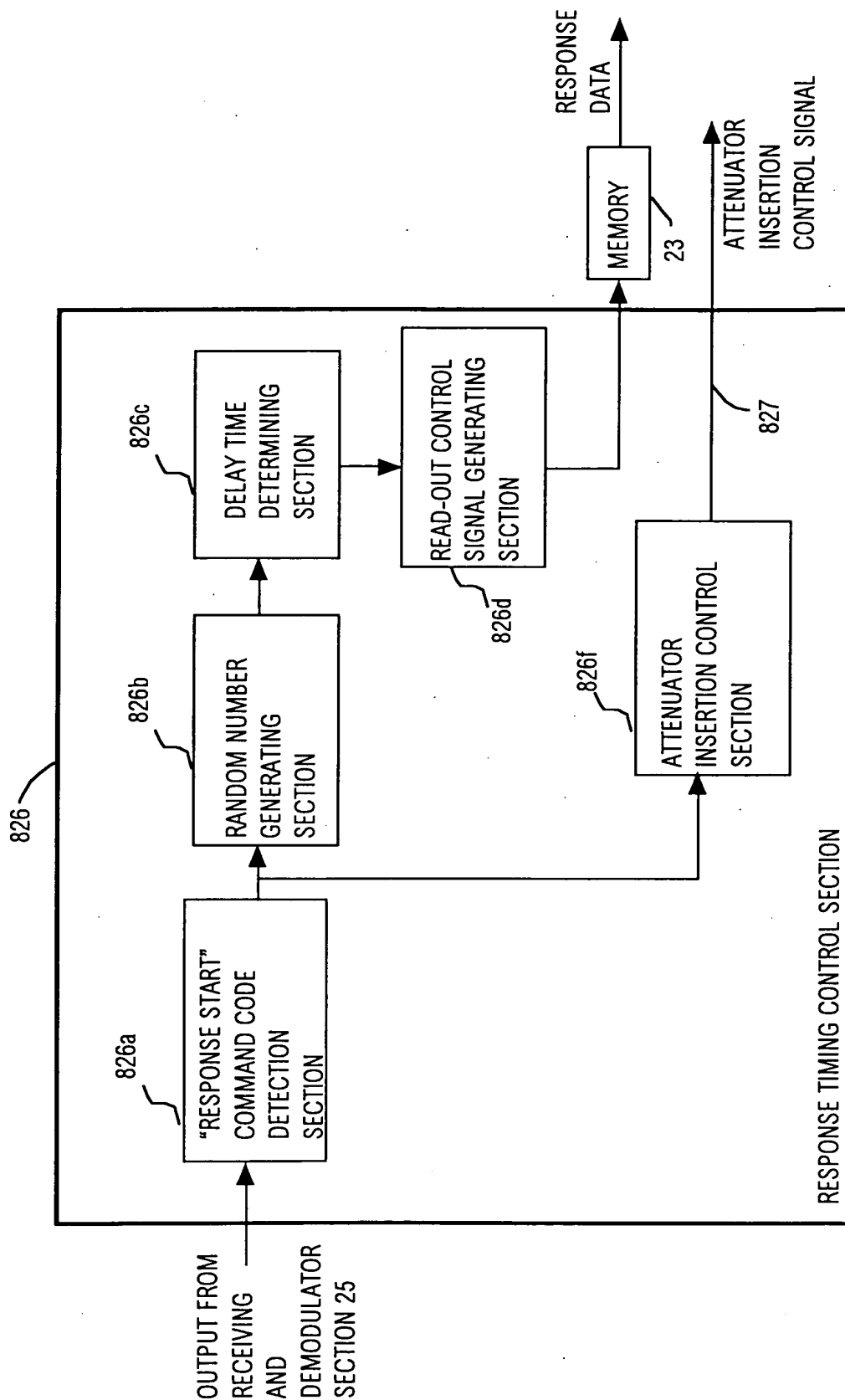
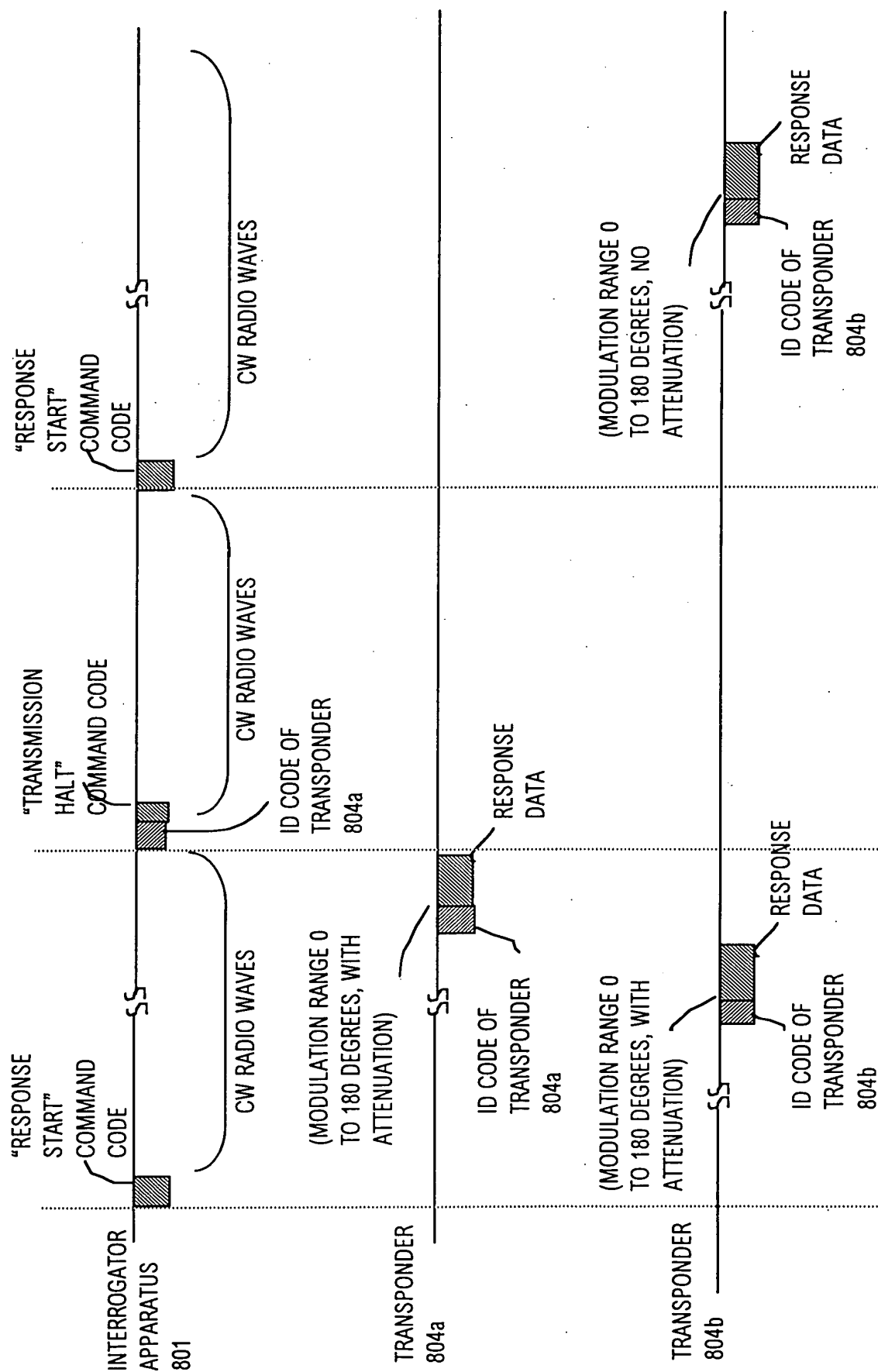


FIG. 48



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FIG. 49



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ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

FIG. 50

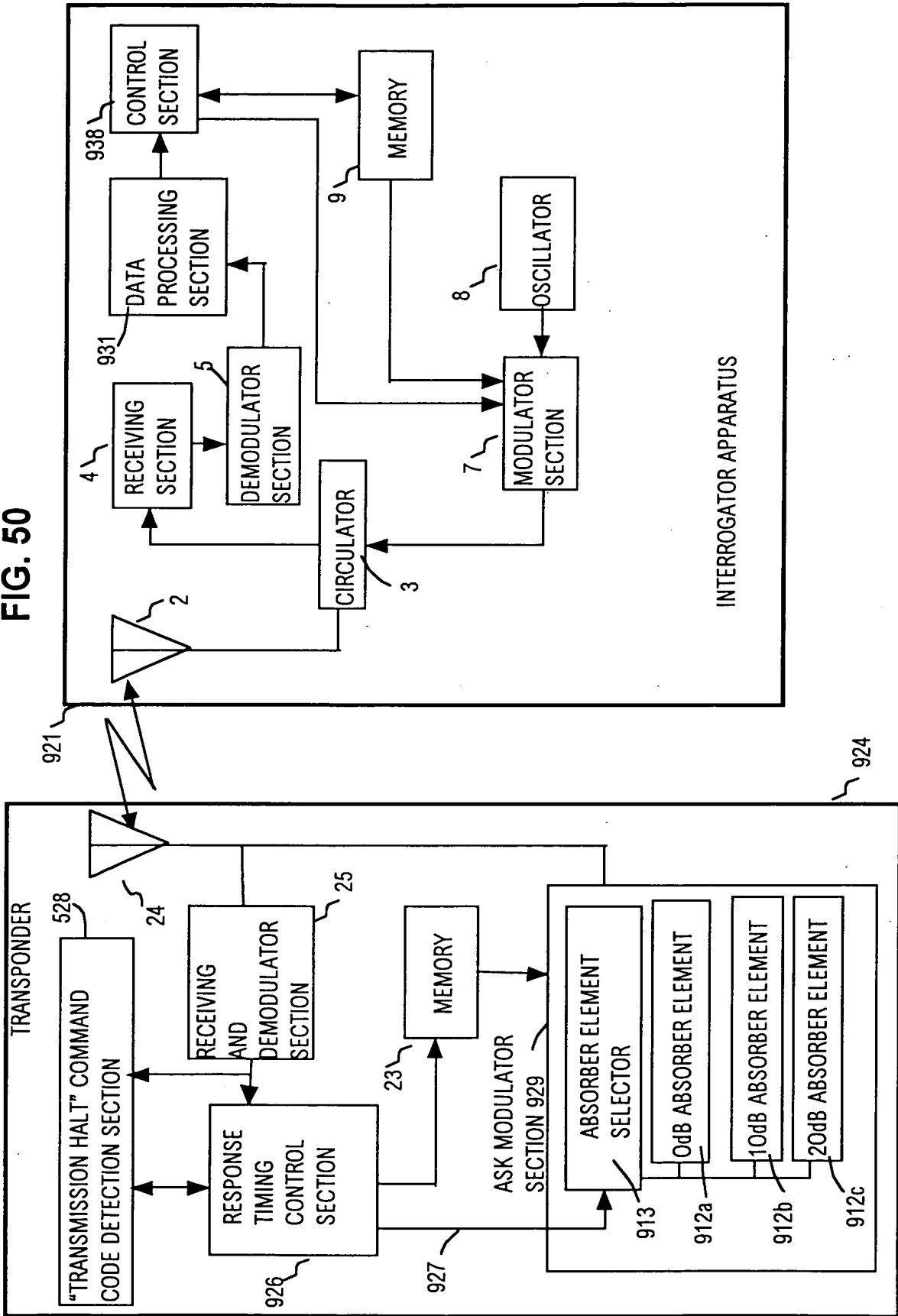


FIG 51

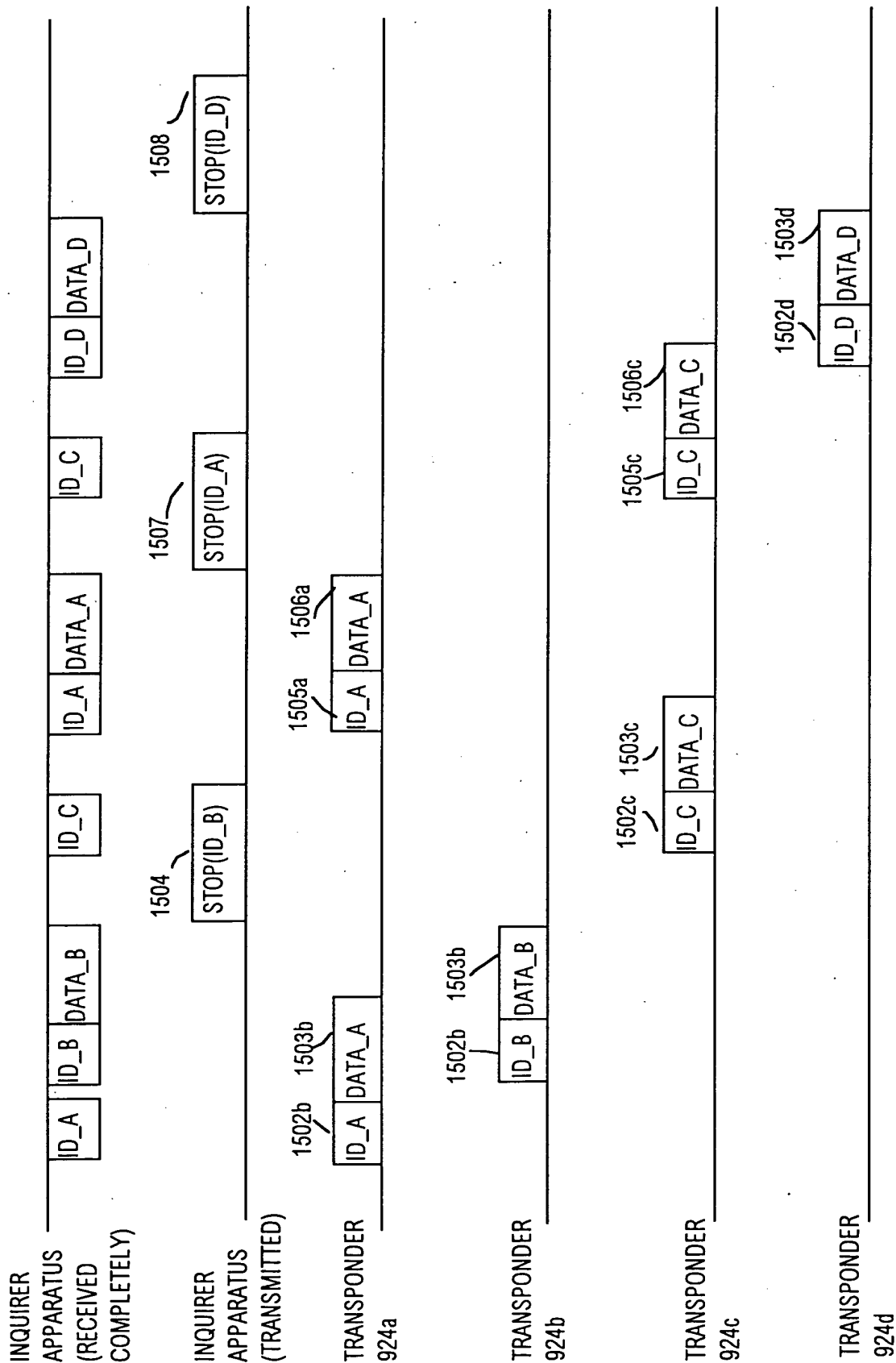


FIG. 52

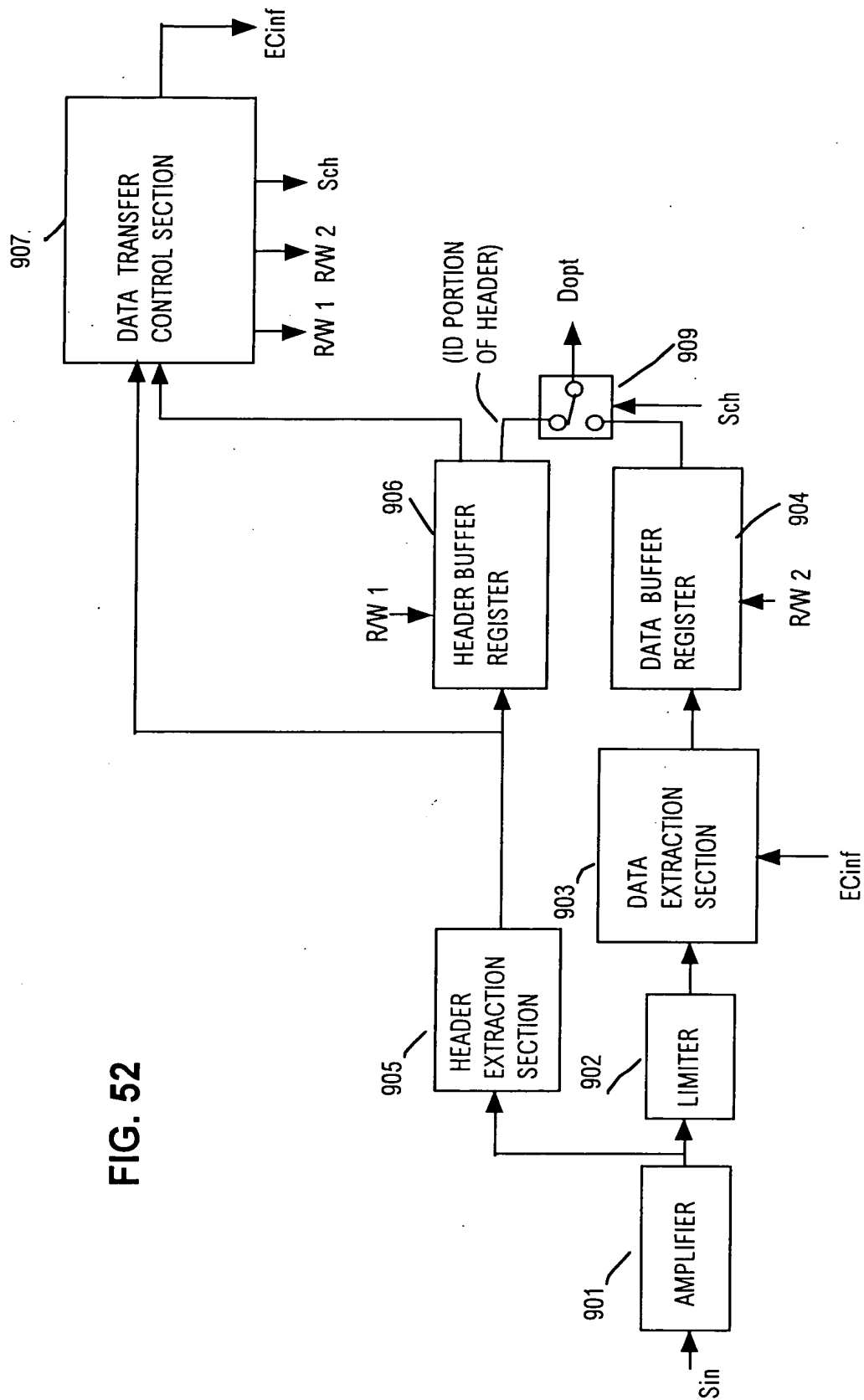


FIG. 53

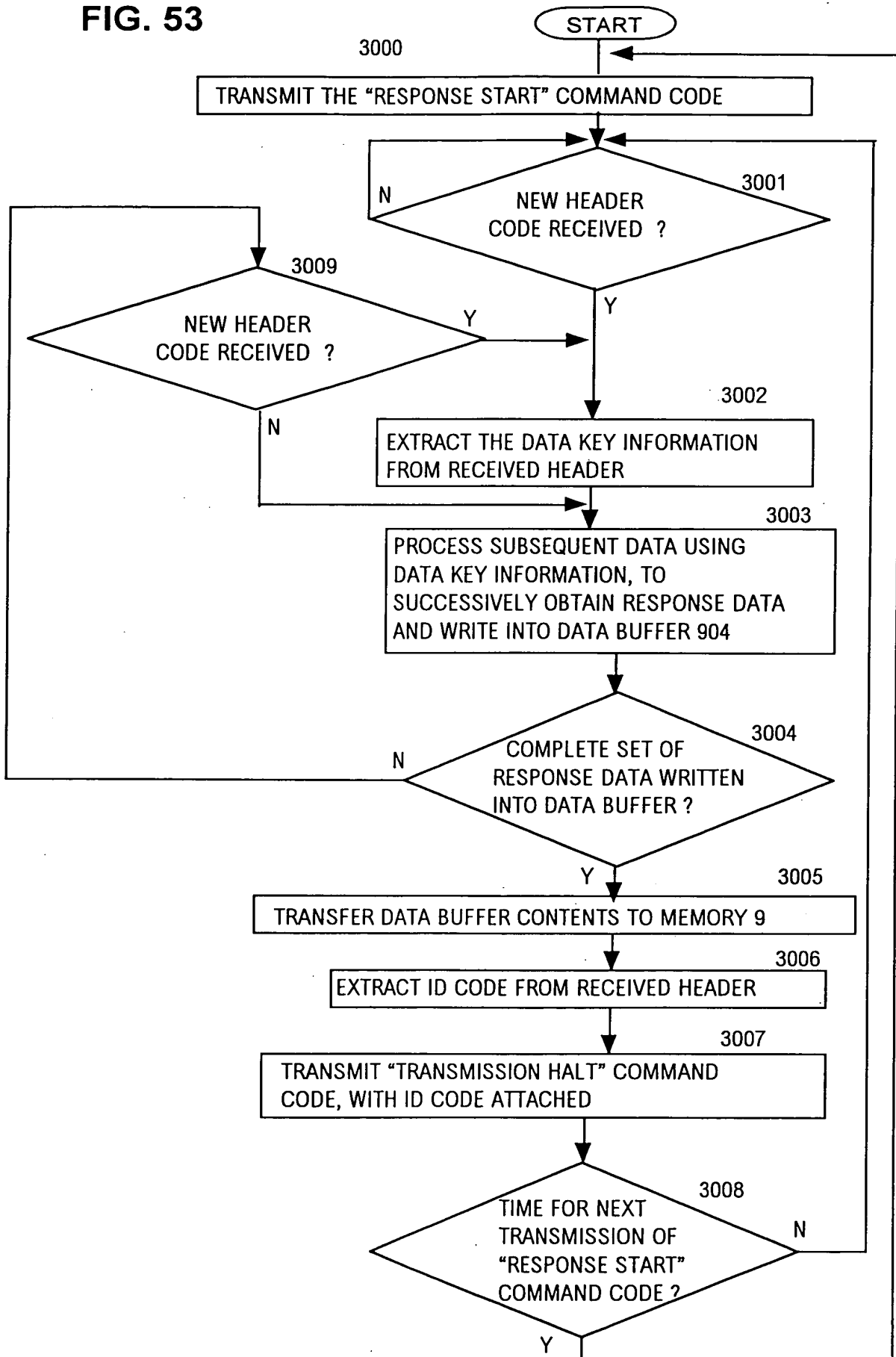


FIG. 54

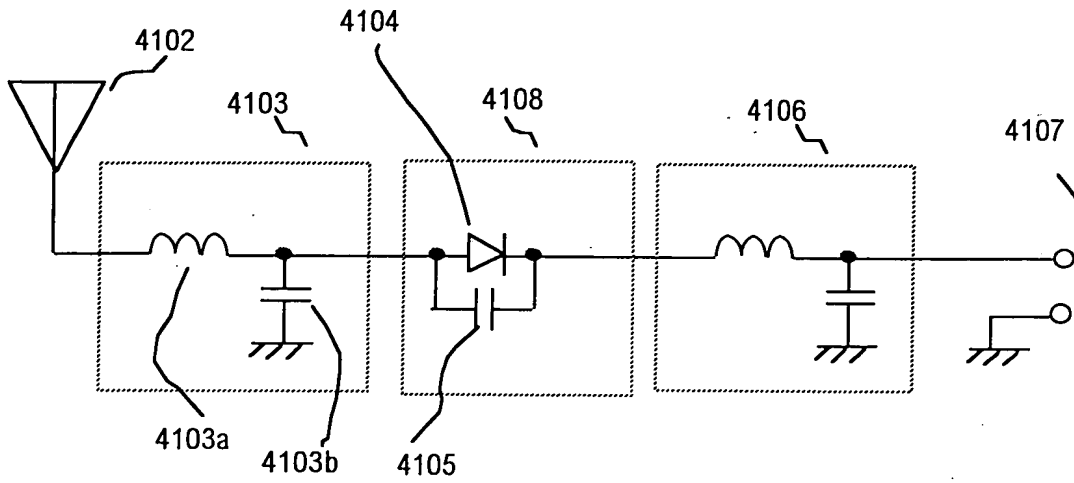
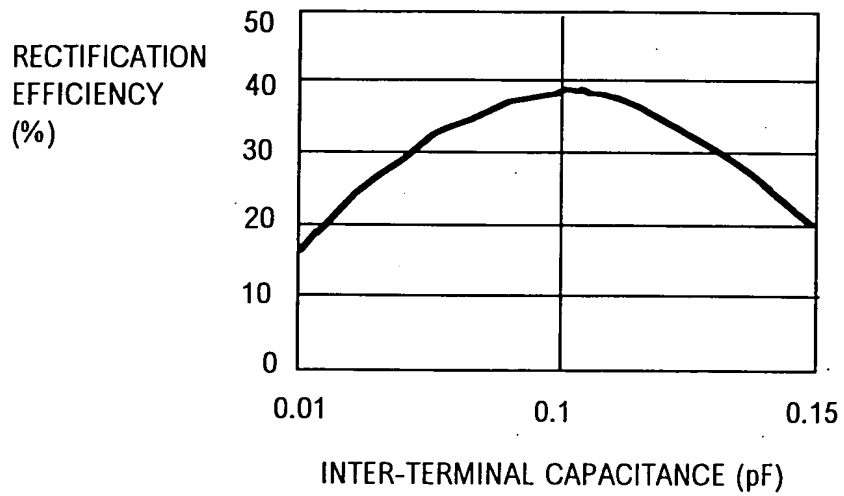


FIG. 55

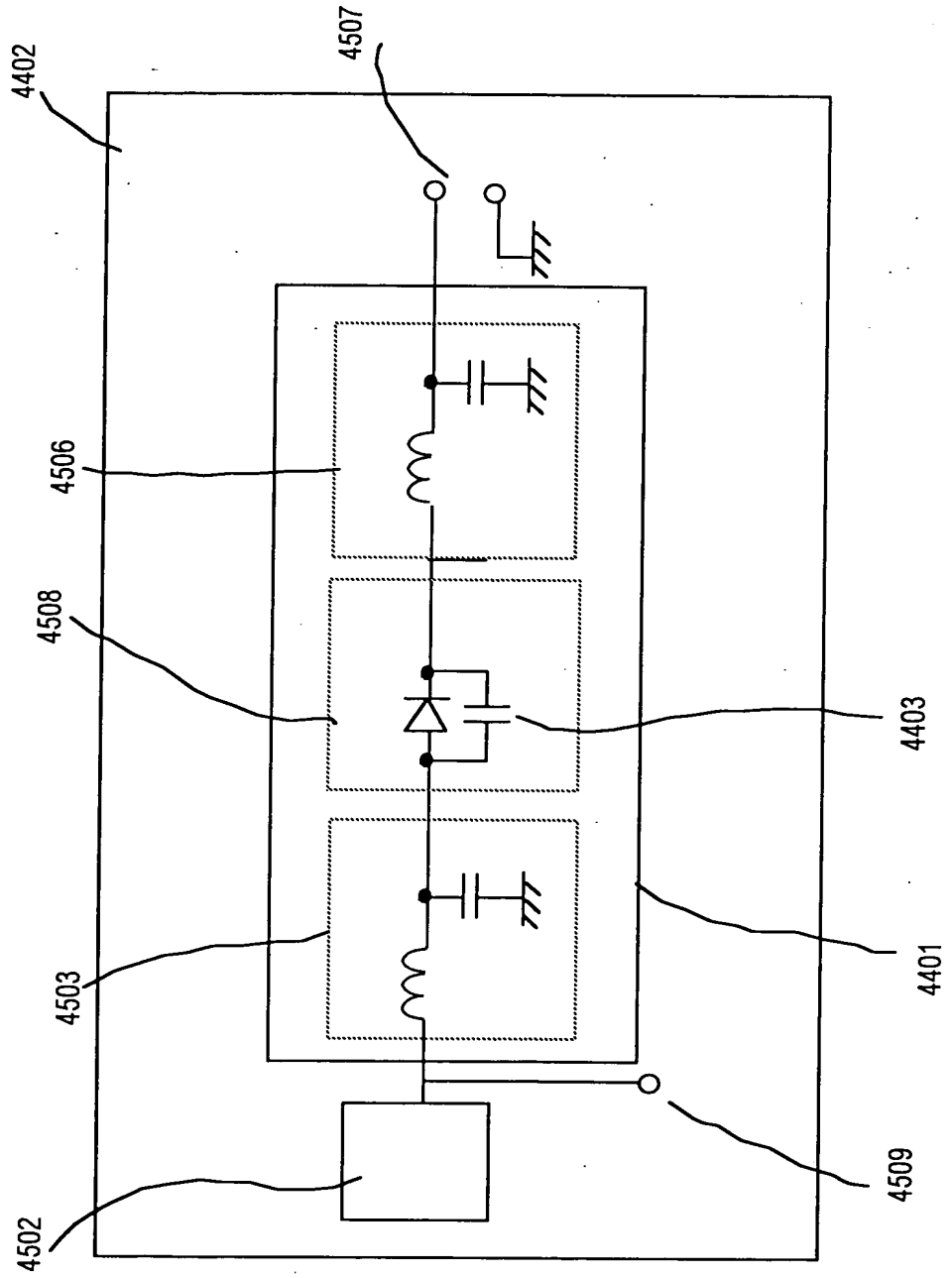


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FIG. 56



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FIG. 57

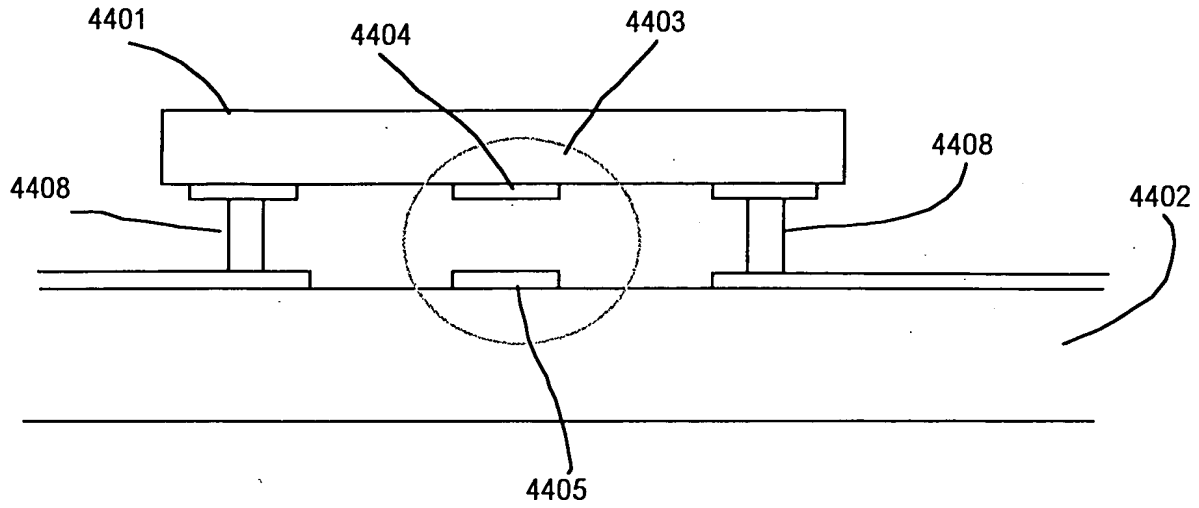
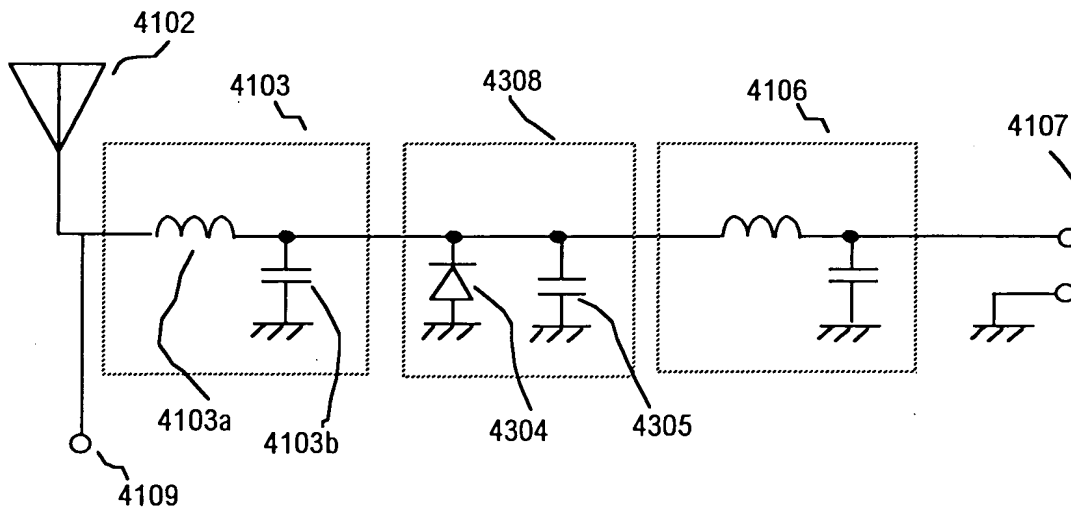
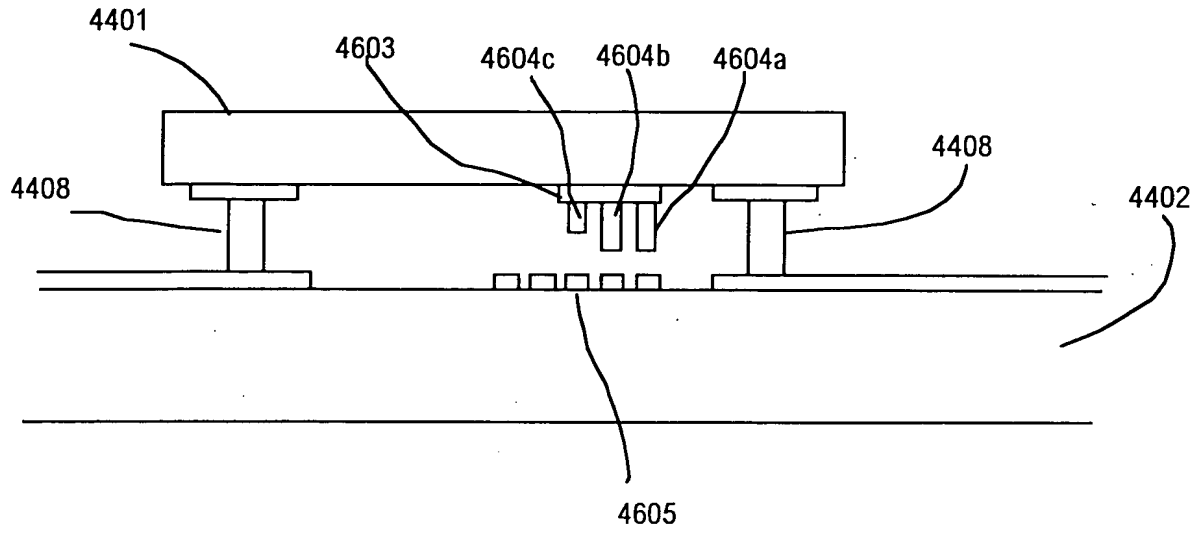


FIG. 58



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FIG. 59



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FIG. 60

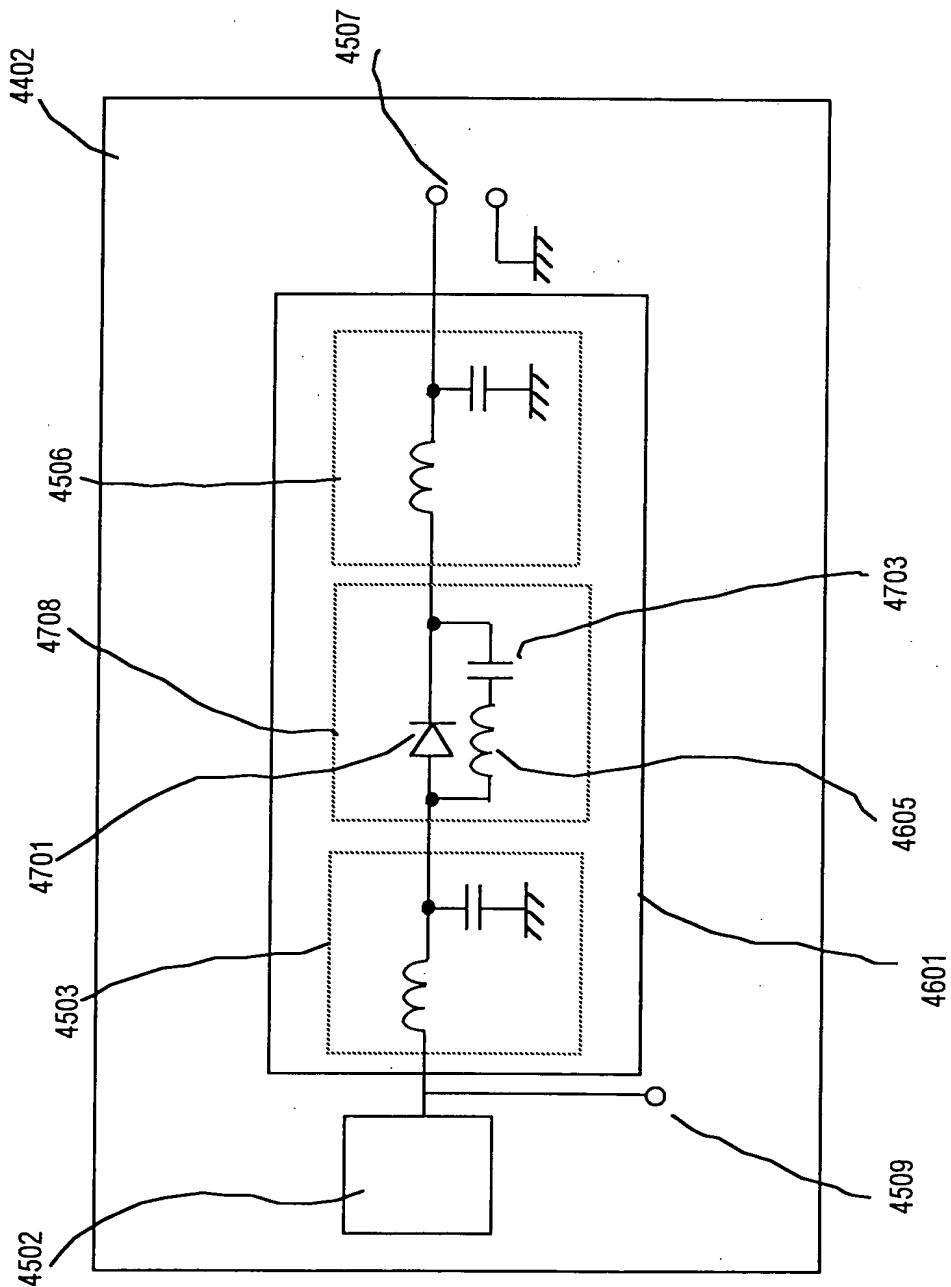
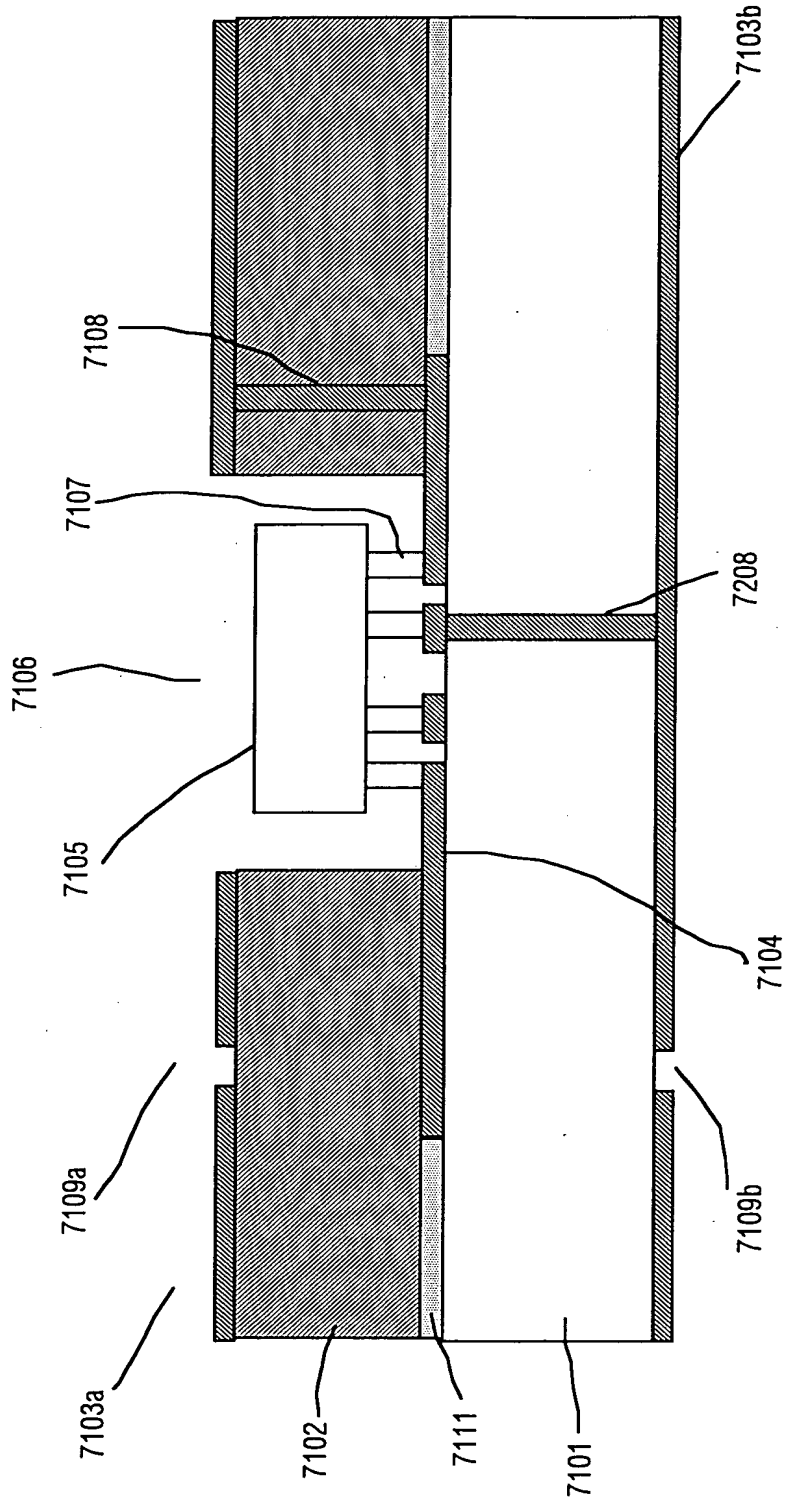


FIG. 61



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FIG. 62

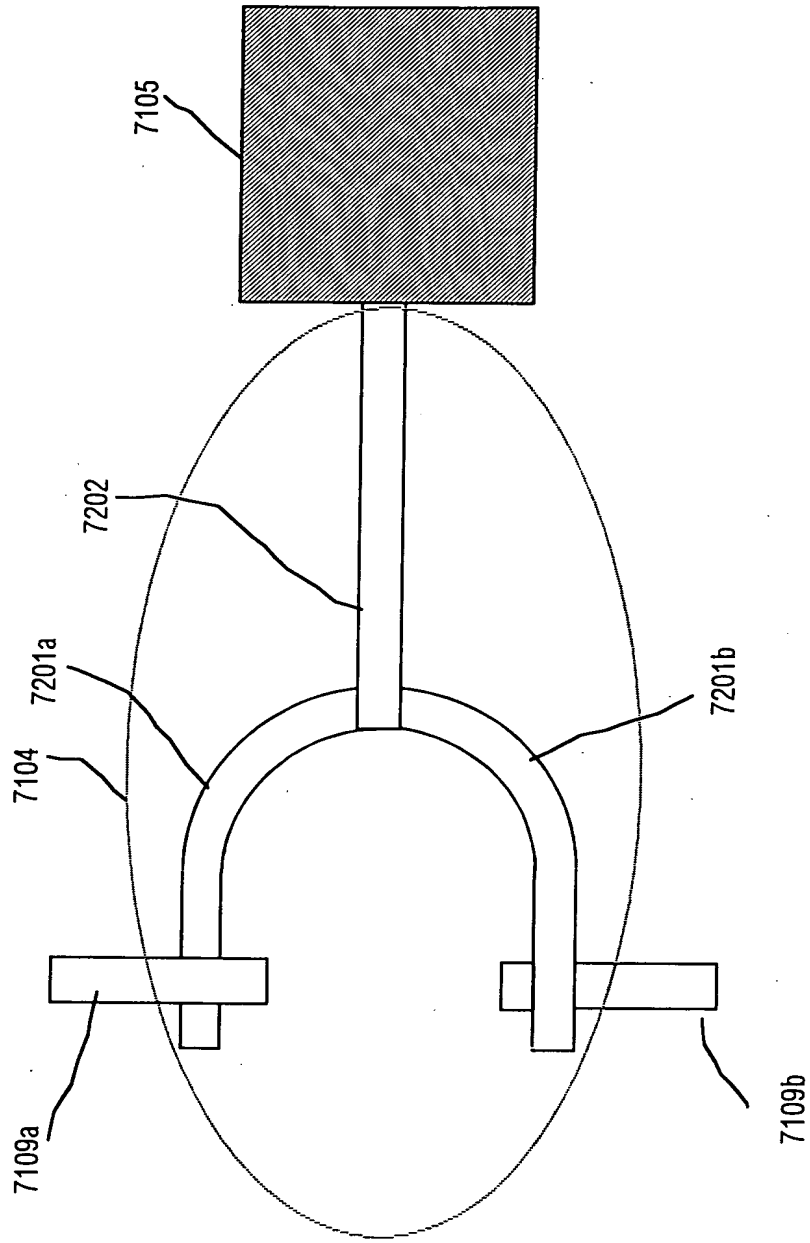


FIG. 63

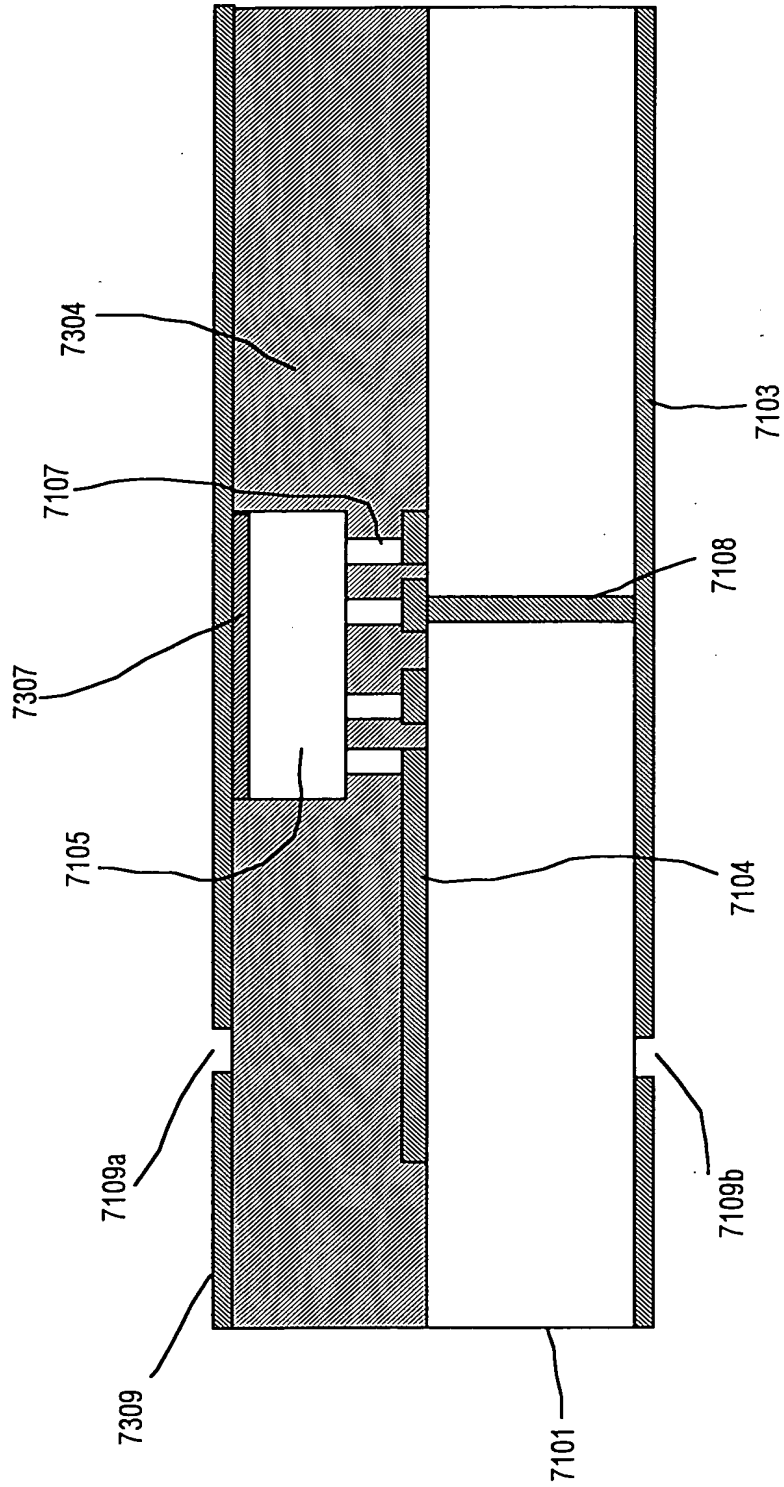


FIG. 64

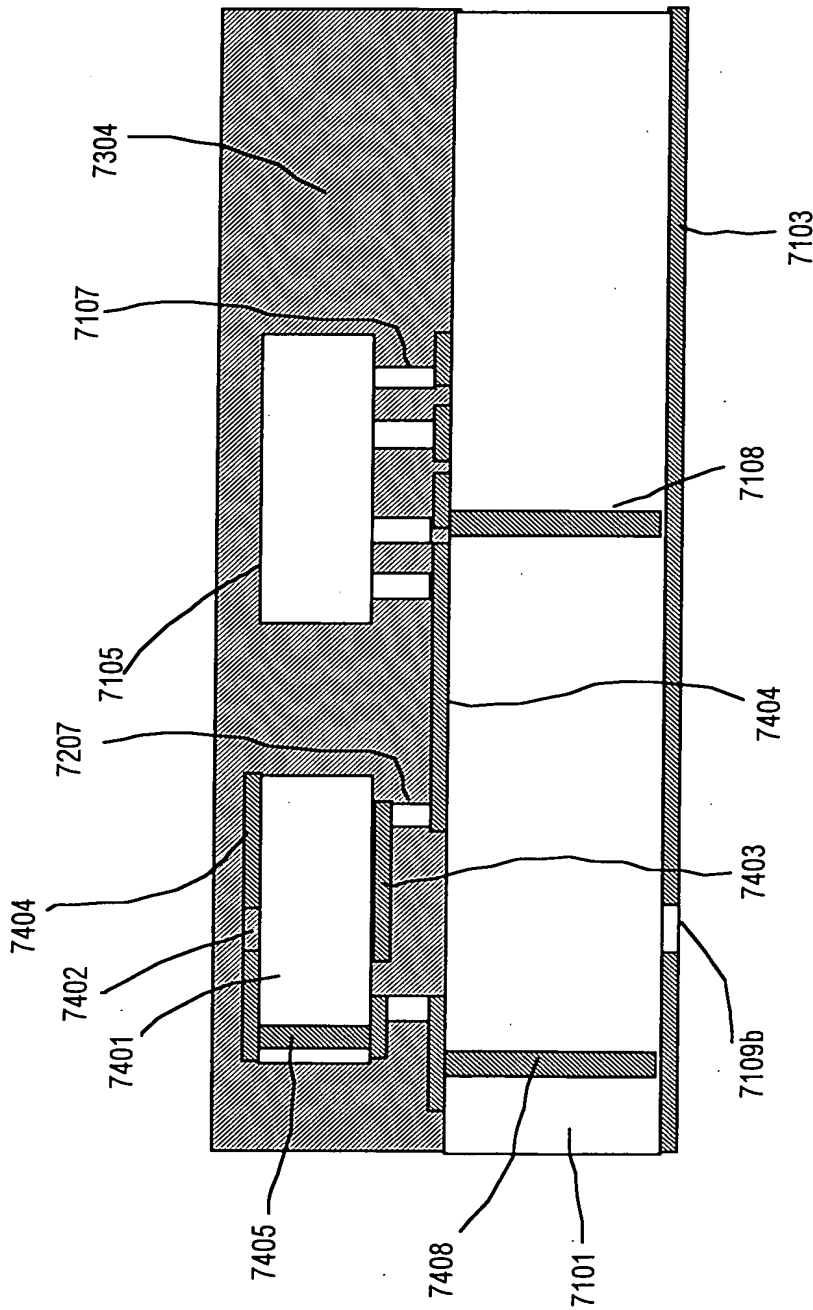


FIG. 65

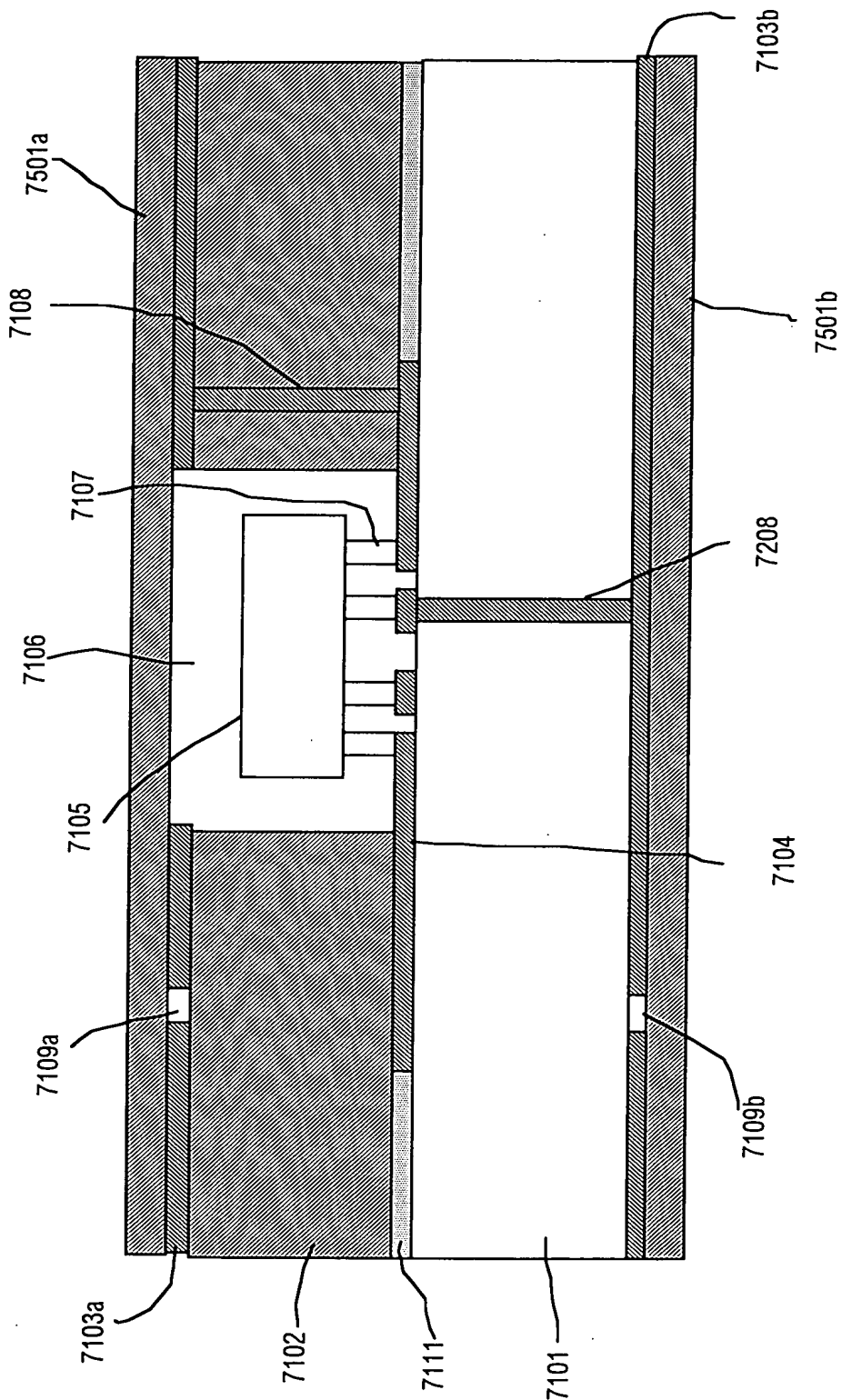


FIG. 66

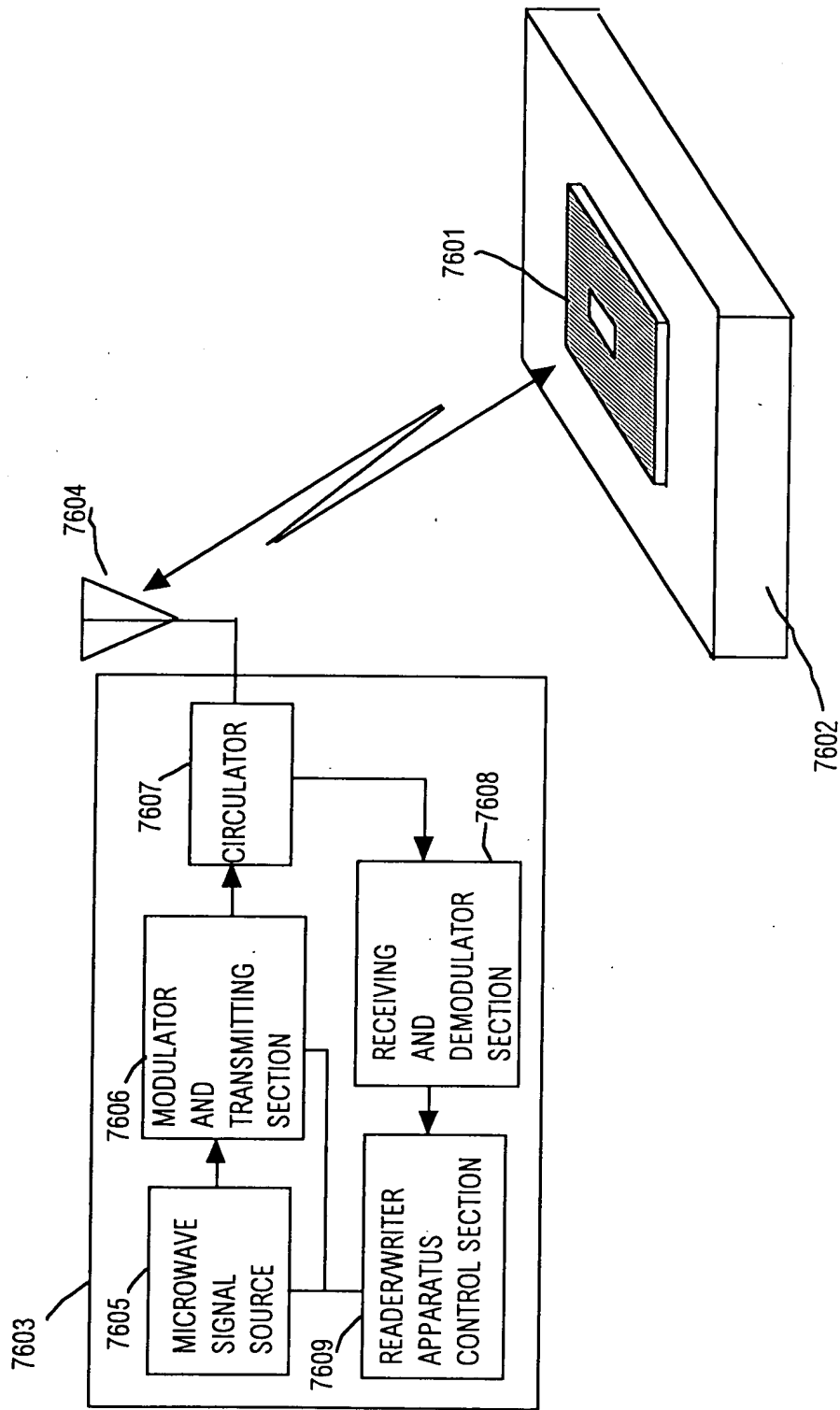


FIG. 67A

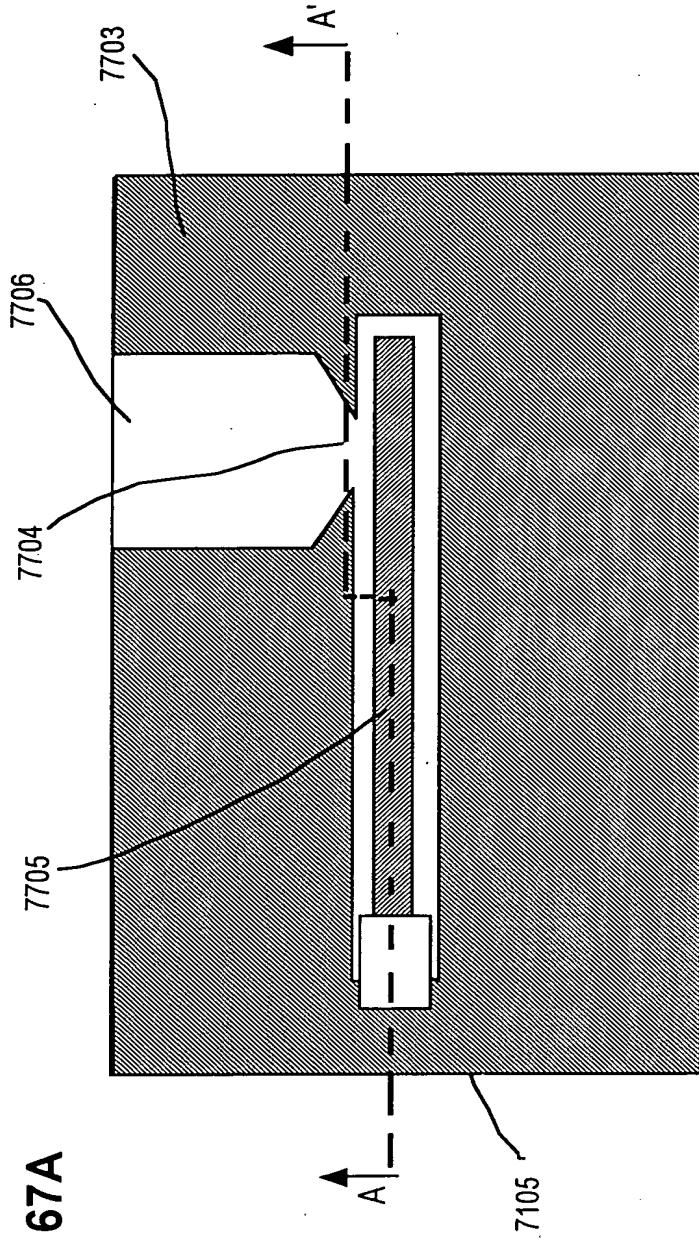
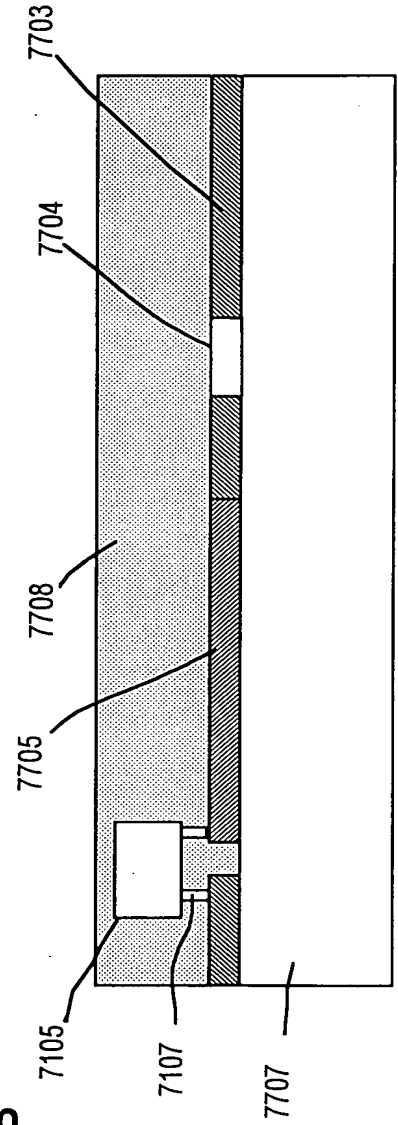


FIG. 67B



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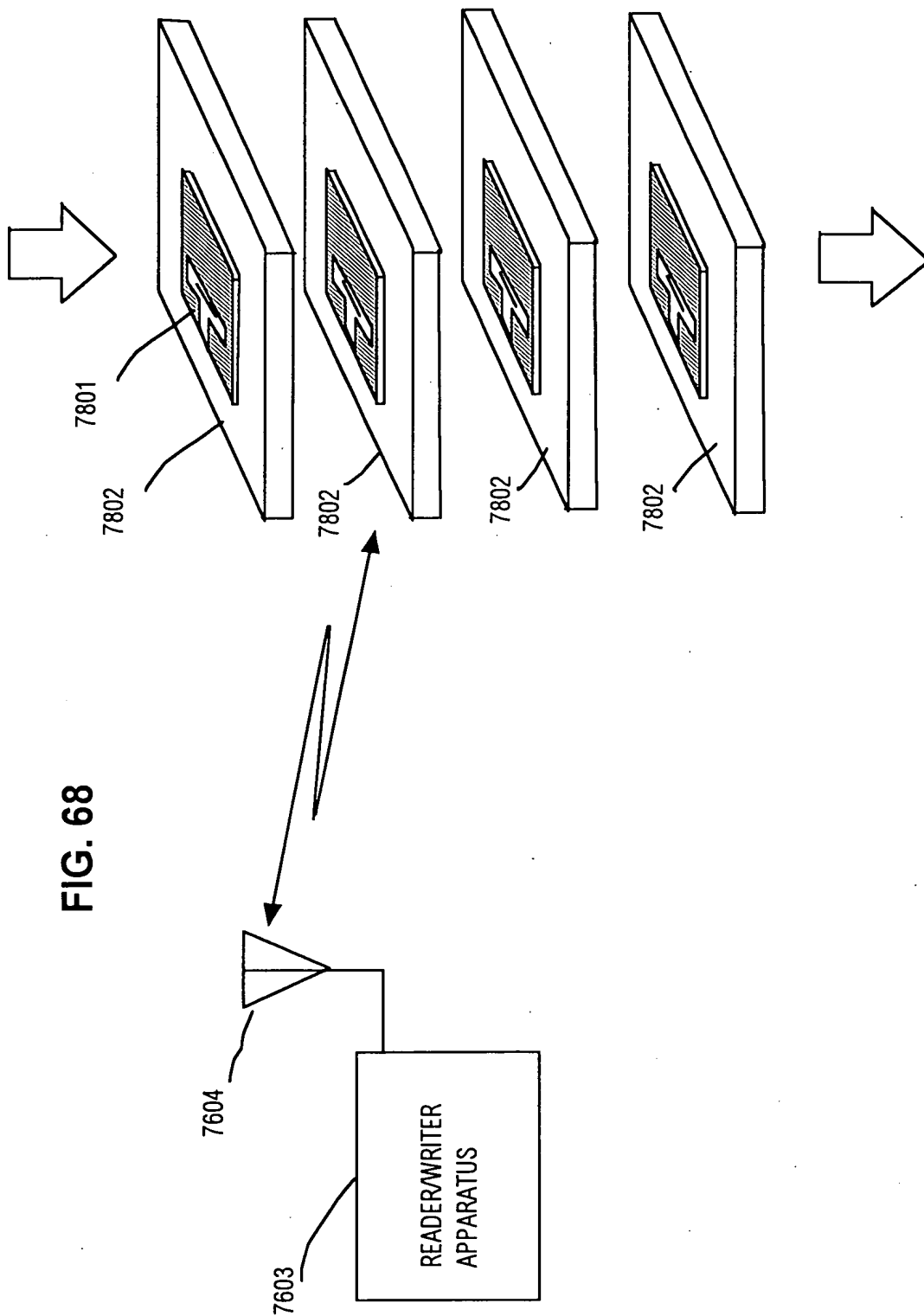
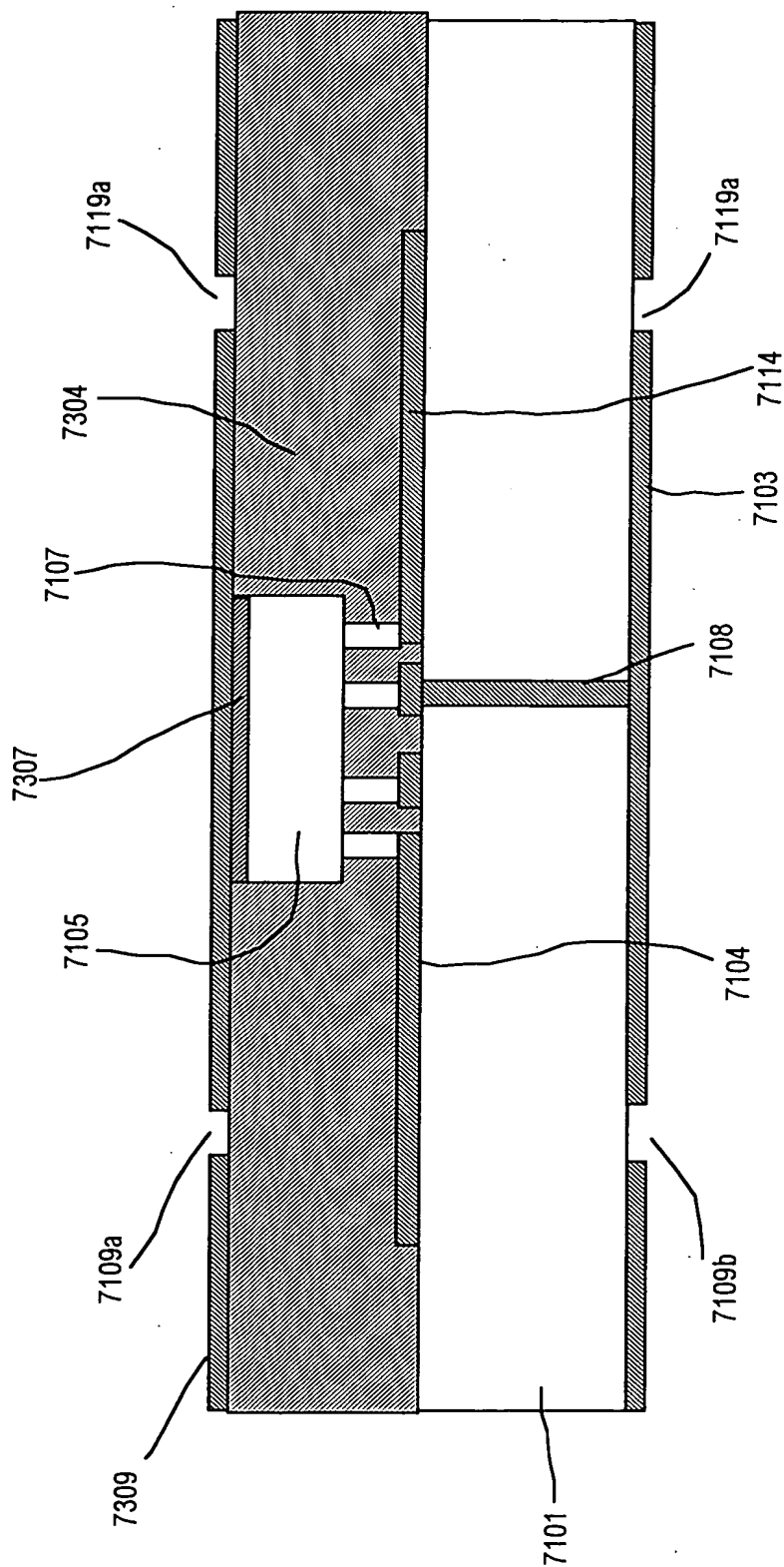


FIG. 69



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FIG. 70

PRIOR ART

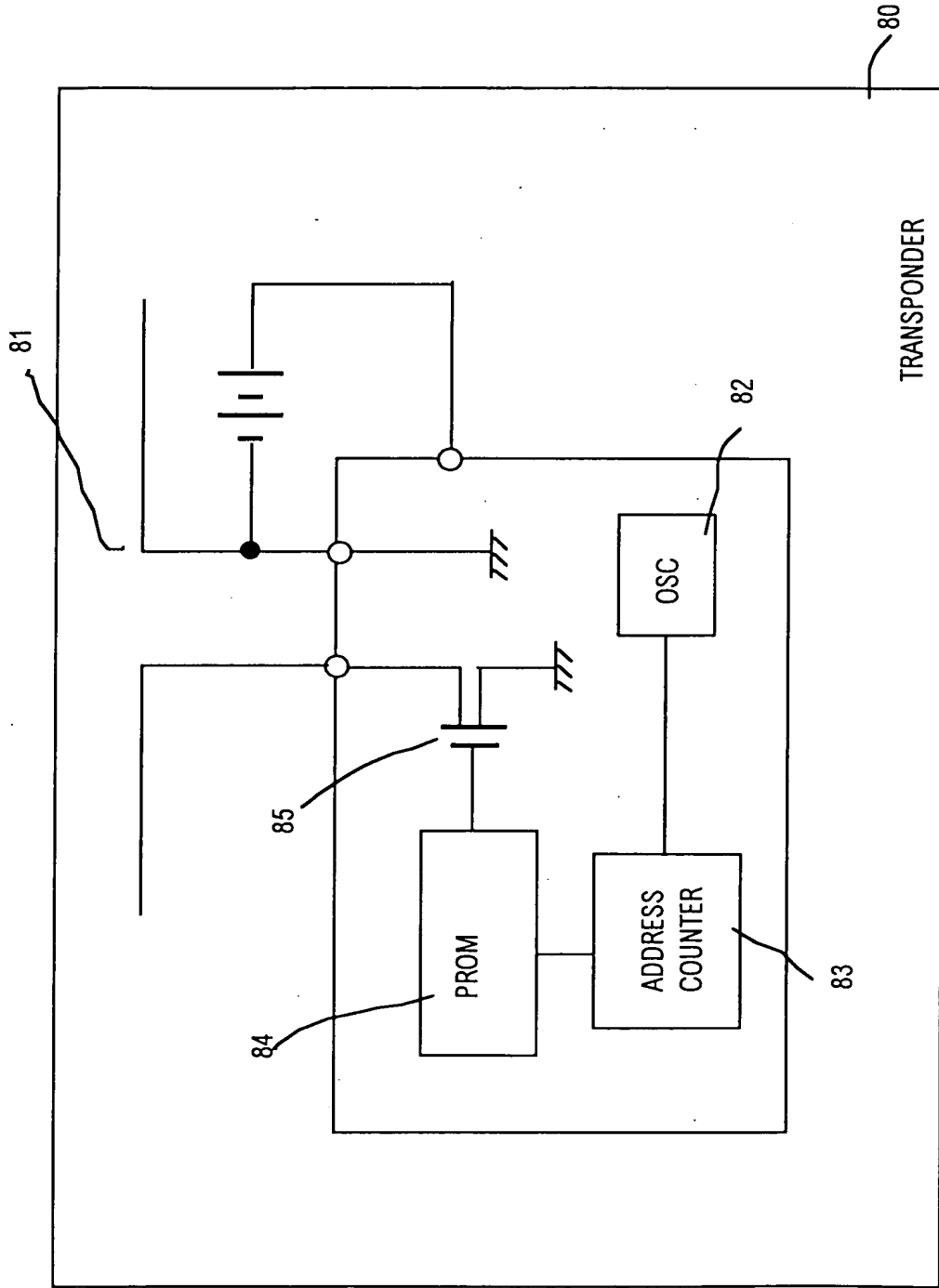


FIG 71

PRIOR ART

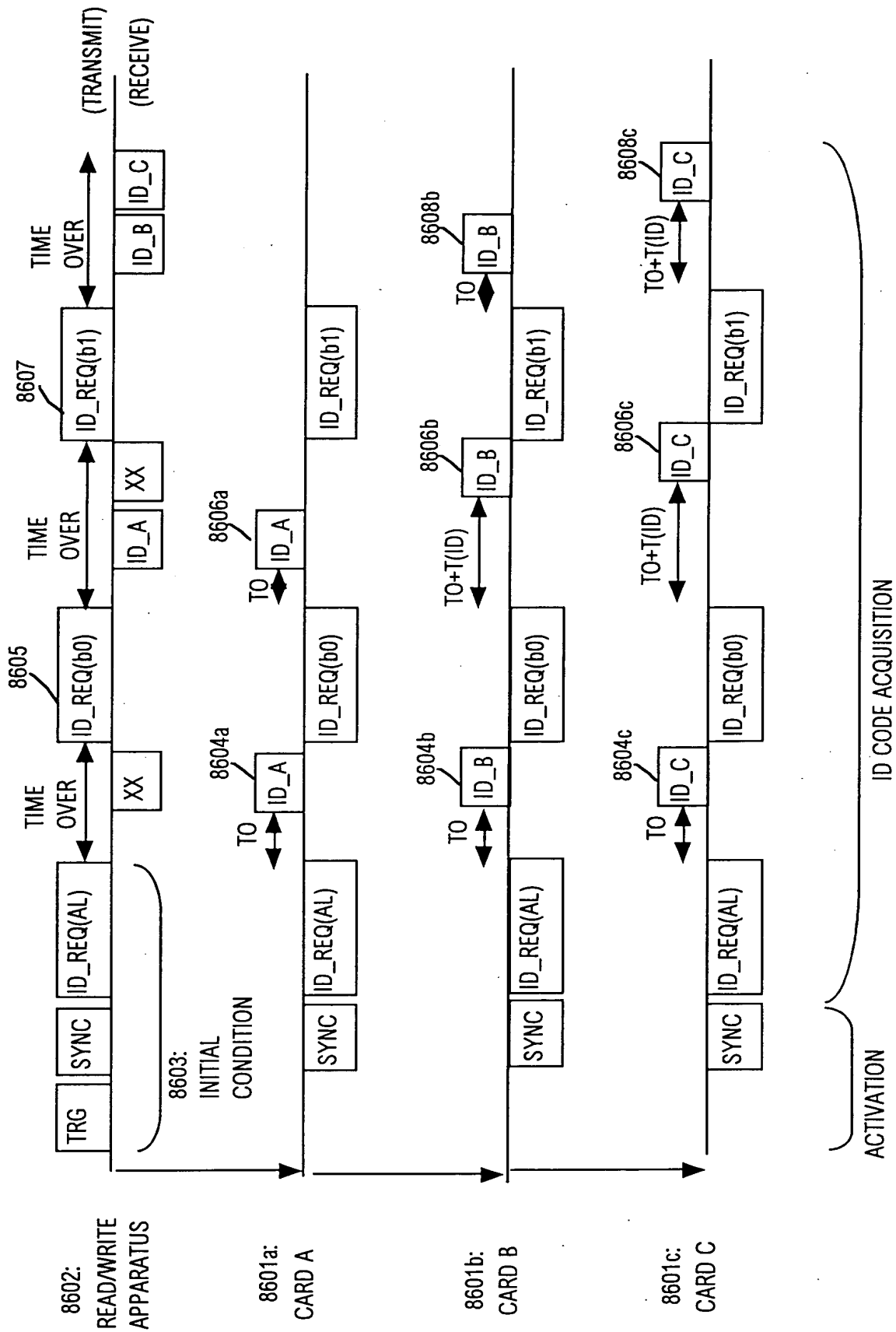
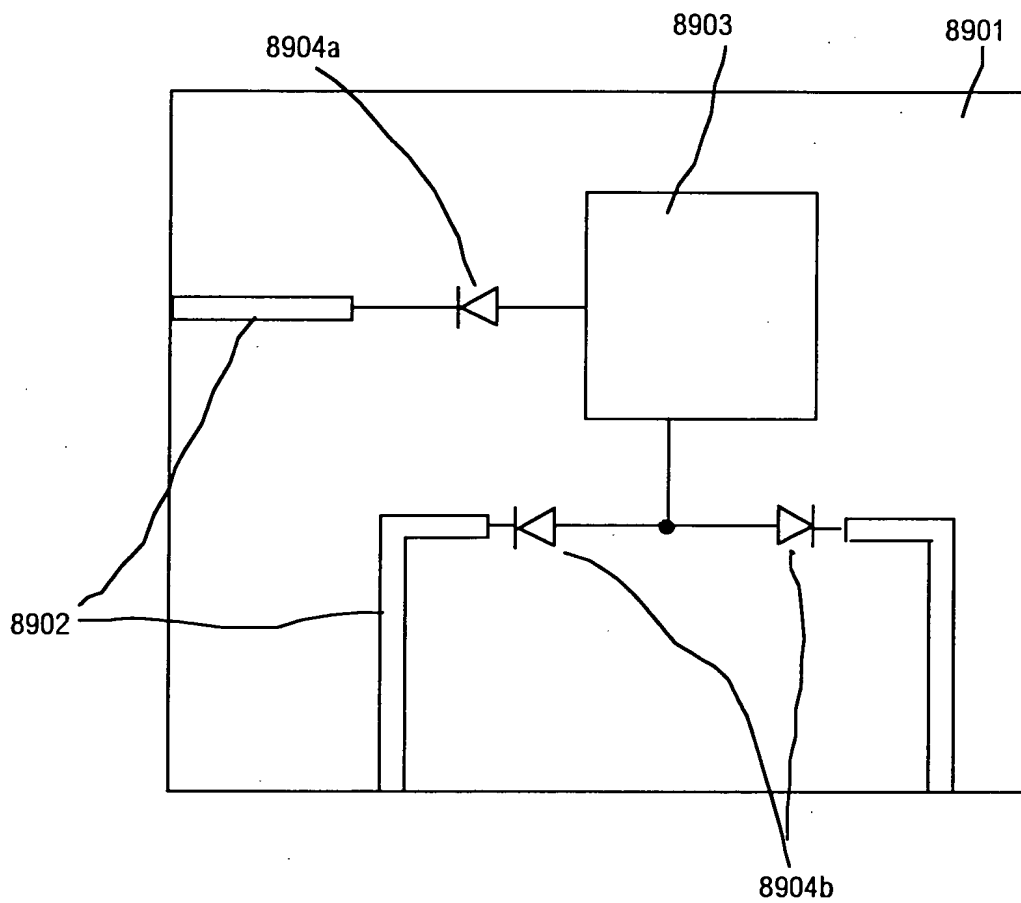


FIG. 72 PRIOR ART



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BY	
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PRIOR ART

FIG. 73

